

Advertisement No. PF-04/2026 dated: 08.05.2026

Online Interview/In Person Interview

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

Candidates are requested to read “[Instruction To Apply](#)” before apply.
Online applications are invited from candidates for the Engagement of Project Staff.

Details of Vacancies & Eligibility

Post code	Project No. & Title	Area/ Group	Name & Number of Positions	Essential Qualification as on the last date of receipt of online applications i.e. 24.05.2026	Age	Consolidated Emoluments Per month (fixed)
3377	Integration of GaN and Diamond Structure for Next-Generation Semiconductor Devices	Diamond Research Group (DRG)	PAT-I/ PAT-II & 01 Note: The Joining Letter will be issued after the availability of funds.	For PAT-I: B.E./ B.Tech./ M.Sc. (Physics/ Applied Physics/ E&C/ Electronics) or equivalent. For PAT-II: B.E./ B.Tech./ M.Sc. (Physics/ Applied Physics/ E&C/ Electronics) or equivalent with 02 years experience.	35 Years	For PAT-I Rs. 25,000/- +HRA For PAT-II Rs. 28,000/- +HRA
3378	Development of Diamond Based Photodetector Array for Solar Blind (UV-C) Imaging Application (Diamond-UVC)	Diamond Research Group (DRG)	PAT-I & 01 Note: The Joining Letter will be issued after the availability of funds.	M.Sc. (Electronics/ Physics/ Applied Physics/ Applied Electronics/ E&I) or equivalent. B.E./ B.Tech. in (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied physics/ Nanotechnology/ Electronics/ VLSI Design/ Instrumentation) or equivalent.	35 Years	Rs. 25,000/- +HRA
3379	MMP015202 & Design and development of portable 3D endoscope of MIS	Applied Electronics Group (AEG)	PAT-I & 01 Note: The Joining Letter will be issued after the availability of funds.	M.Sc./ B.E./ B.Tech. in electronics/ electrical/ computer science engineering or equivalent. Desirable Skills: Microcontroller based system design, multi-layer pcb design, web and android based software packages design, AI and ML based solutions.	35 Years	Rs. 25,000/- + HRA
3380	AI-Assistive Digital Probe for High-Resolution Turbidity Measurement in Drinking Water Samples	Applied Electronics Group (AEG)	Project Assistant-I: 01 & Project Assistant-II: 01 & PAT-I(With GATE/ NET): 01	For Project Assistant-I: 10 th Pass/ ITI in electronics or equivalent (Desirable: Skillset in soldering assembling of SMD components) For Project Assistant-II: B.Sc. / 3 year Diploma in engineering/ technology (Desirable: Skillset in microcontroller	35 Years	For Project Assistant-I: Rs. 18,000/- +HRA For Project Assistant-II: Rs. 20,000/- +HRA

			<p>&</p> <p>PAT-I(Without GATE/NET): 01</p> <p>Note: The Joining Letter will be issued after the availability of funds.</p>	<p>programming and debugging)</p> <p>For PAT-I (Without GATE/NET): B.E./ B.Tech. in electronics, computer science or equivalent without GATE/ NET (Desirable: HMI based system design, Microcontroller based system design, python, JAVA, web and android app design.)</p> <p>For PAT-I(With GATE/NET): B.E./ B.Tech. in electronics, computer science or equivalent with or with 01 year experience GATE/ NET (Desirable: HMI based system design, Microcontroller based system design, python, JAVA, web and android app design.)</p>		<p>For PAT-I(With GATE/ NET): Rs. 31,000/- +HRA</p> <p>For PAT-I(Without GATE/NET): Rs. 25,000/- +HRA</p>
3381	CLP-3467 & Design and Development of an Indigenous Tumour Ablation Device	Advanced Information Technologies Group (AITG)	<p>PAT-II: 02 & Project Assistant-I: 01</p> <p>Note: If no candidate is found suitable for PAT-II, PAT-I may be considered for the position.</p>	<p>For PAT-II: B.Tech. in CS/ IT/ ECE/ Robotics/ EEE/ Mechanical/ Mechatronics/ Instrumentation or equivalent and 02 years of experience in industrial/ academic or R&D department. M.Tech in CS/ IT/ ECE/ Robotics/ EEE/ Mechanical/ Mechatronics/ Instrumentation or equivalent. Desirable: Proficiency in embedded C, PCB design, RF circuits, software development and HMI design.</p> <p>For Project Assistant-I: SSC/ 10th + ITI</p>	35 Years	<p>For PAT-II Rs. 35,000/- Or Rs. 28,000/- +HRA For Project Assistant-I Rs. 18,000/- +HRA</p>
3382	GAP-3423 & Multilevel capacity building and skilling in industry-aligned emerging technology	Societal Electronics Group (SEG)	<p>Project Assistant-II & 01</p> <p>Note: The Joining Letter will be issued after the availability of funds.</p>	<p>03 Years Diploma in Engineering/ Technology (Electronics). Preferable: Experience in electronic circuit, system design and development of societal applications.</p>	35 Years	Rs. 20,000/- +HRA
3383	GAP-3424 & Advanced integrated system combining DC-DC converter and on-board charger for two wheel electric vehicles	Societal Electronics Group (SEG)	<p>Project Associate-I & 01</p> <p>Location: CSIR-CEERI, Chennai Centre, Chennai</p> <p>Note: The Joining Letter will be issued after the availability of funds.</p>	<p>Four years B.Tech./ B.E in Electrical/ Power Electronics/ Electrical and Electronics/ Electronics/ Electronics and Communication. M.Sc. in Electronics or equivalent from recognized university or equivalent. Preferable: Experience in Electrical Engineering, Two wheel electric vehicles, Power electronics, System design, Integrated circuit design flow, Hardware accelerators, HDL modeling and EDA tools such as cadence, Xilinx, AutoCAD etc.</p>	35 Years	Rs. 37,000/- Or Rs. 30,000/- as per ANRF/OM/N-01/2024
3384	Smart automation of electrically resilient large-area CVD technology for scalable industrial synthesis	Applied Electronics Group (AEG)	<p>PAT-I & 01</p> <p>Note: The Joining Letter will be issued after the</p>	<p>B.E./ B.Tech./ M.Sc (ECE/ EEE/ EE/ Instrumentation/ Electrical/ Electronics/ Power Engineering/ Renewable Energy) or equivalent. Desirable: Working experience and domain knowledge of areas of PLC automation/ embedded systems.</p>	35 Years	Rs. 25,000/-

			availability of funds.			
3385	Development of an indigenous embedded control and automation system for atomic layer deposition (ALD) for thin film research	Applied Electronics Group (AEG)	PAT-I & 01 Note: The Joining Letter will be issued after the availability of funds.	B.E./ B.Tech./ M.Sc (ECE/ EEE/ EE/ Instrumentation/ Electrical/ Electronics/ Power Engineering) or equivalent. Desirable: Working experience and domain knowledge of areas of PLC automation/ embedded systems.	35 Years	Rs. 25,000/-
3386	Smart pavement tiles for scavenging energy from footfalls through hybrid nanogenerators enabling sustainable walkways	Applied Electronics Group (AEG)	Senior PAT & 01 Note: The Joining Letter will be issued after the availability of funds.	B.E./ B.Tech./ M.Sc (ECE/ EEE/ EE/ Instrumentation/ Electrical/ Electronics/ Power Engineering/ Renewable Energy) or equivalent and 03 years of experience in Research and Development in Industrial/ Academic institutions or science and technology organizations. Or Masters/ Integrated Masters in Engineering or technology(Power Electronics/ Renewable Energy/ Embedded systems/ Power systems) Desirable: Working experience and domain knowledge of areas of power electronics.	40 Years	Rs. 42,000/-
3387	GAP-6526 & Design and Development of Three-Axis MEMS-based Capacitive Accelerometer Module	Semiconductor Sensors and Microsystems Group (SSMG)	JRF & 01 Note: The Joining Letter will be issued after the availability of funds.	M.Sc. (Electronics/ NanoScience/ Nano Electronics/ Physics/ Applied Physics/ Applied Electronics/ E&I Or BE/ B.Tech/ ME/ M.Tech in (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ VLSI/ Microelectronics/ Nanotechnology/ MEMS) or equivalent; Selected through a process described through any one of the following: a) Scholars who are selected through National Eligibility Tests-CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc.	30 Years Note: 5 years of relaxation for OBC-NCL, SC, ST, PwD, Third Gender candidates , and women.	Rs. 37,000/- +HRA

*Candidates with above qualification without experience can also apply they shall be eligible for the position of PAT-I

For PAT – I:

(i) ₹31,000/- to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc and IISER etc.

(ii) ₹25,000/- for others who do not fall under (i) above.

* For PAT – II:

(i) ₹35,000/- to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹28,000/- for others who do not fall under (i) above

For AcSIR Ph.D. (Engineering/IDDP) Students

1. Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.
2. Those who are having fellowship, they are not eligible to apply.

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

For Recruitment queries:

Sh. Sanjay Khangar
Section Officer
CSIR-CEERI, Pilani
Email: recruitment.ceeri@csir.res.in
Phone: 01596-252275

For Technical queries:

Sh. Jagmohan Pathak
Advanced Information Technologies Group
CSIR-CEERI, Pilani
Email: pathakjt.ceeri@csir.res.in
Phone: 01596-252282

How to Apply:

Read the instruction given on the CSIR-CEERI Website on Project Staff Recruitment Page.

Or Given the link in blue before the table in this advertisement.

Other Conditions:

1. Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Associate-I(PAT-I), Project Associate-II(PAT-II), Senior PAT, Junior Research Fellow (JRF), Project Assistant-I and Project Assistant-II for a total period of 6 years or more are not eligible for these engagements. The candidates who have served for a period less than 6 years will have tenure up to remaining period till completion of Six years.
2. Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Project Associate-I(PAT-I), Project Associate-II(PAT-II), Senior PAT, Junior Research Fellow (JRF), Project Assistant-I and Project Assistant-II shall not exceed 6 years in any case. The total tenure of Six years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as Project Associate-I(PAT-I), Project Associate-II(PAT-II), Senior PAT, Junior Research Fellow (JRF), Project Assistant-I and Project Assistant-II or as any other designation of equal status.
3. **All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**
4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Project Associate-I(PAT-I), Project Associate-II(PAT-II), Senior PAT, Junior Research Fellow (JRF), Project Assistant-I and Project Assistant-II will be made on behalf of the Sponsor of the Projects. These are

not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.

6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PWBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
7. The relaxation in the age limit for the candidates belonging to the category of SC/ST/PwBD/Women and the category of OBC shall be 05 years and 03 years respectively or as per Govt. of India instructions issued in this regard from time of time. (CSIR OM No. 5-1(342)/2017-PD dated September 02,2024.)

8. Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:

- a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
 - i. 10th Standard
 - ii. 12th Standard
 - iii. Diploma Degree
 - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
 - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DOB such as 10thStandard, DOB Certificate, etc.)
- c. Category certificate (in case of SC/ ST/ OBC/ PWBD/ EWS candidates)
- d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)
- e. Experience certificate (Mandatory for the posts where experience is essential qualification)
- f. Other certificates (If applicable)
9. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
10. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
11. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.
12. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
13. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.
14. The number of positions may be increased or decreased as per the requirements.
15. HRA will be admissible as per CSIR guidelines and subject to availability of funds in the project.
16. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the

engagement of above Project Staff as amended from time to time.

The last date & time for applying applications is 24.05.2026 till 05:00 PM. Application received after last date will not be entertained.

**Sd/-
Section Officer**

