

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI

No. 4/2-3(5)/2025-E-I

Date: 13.02.2026

OFFICE MEMORANDUM

Sub :- Result for the post of PAT-I.

Ref: CSIR Letter dated 02.09.2024 No. 5-1(342)/2017-PD: *Guidelines for engagement of project personnel under various CSIR (incl. NMITLI, OLP, MLP, industry sponsored, Contract/ Consultancy / Sponsored projects etc) and non-CSIR funded projects - reg. (clause 4.4)*

Based on the recommendations of the Selection Committee & performance of candidates in Interview held in hybrid mode, the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement of PAT-I.

The details are as under: -

Name of Position: PAT-I

Project No.: IHP202404

Number of Position: 01

S.No.	Project No.	Name	Name of Position	Emolument(s)
1.	IHP202404	Ashish Saini	PAT-I	₹25,000/-

RSB
13/02/2026

SECTION OFFICER