

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE,
PILANI (RAJASTHAN)-333031

No. 6(14)/05-Estt.


Dated: 13.11.2025

OFFICE MEMORANDUM

The Director, CSIR-CEERI has been pleased to approve the transfer/posting as under for smooth functioning of operations at the institute in public interest:

| S. N. | Name & Designation | Current Place | New Place |
|-------|---|---------------------|---|
| 1 | Sh. Gyan Singh Meena, Senior Technical Officer (2) | CEERI Jaipur Centre | Institute Facility Group, CEERI Pilani |

The aforesaid officer will join his respective place of posting immediately and in any case not later than 31 Dec. 2025.


(Madhuranjan Pandey)
Controller of Administration

Copy to:

1. PA to Director
2. PA to COA
3. Sh. Gyan Singh Meena, STO (2)
4. Scientist-in-Charge, CSIR-CEERI Jaipur Centre
5. Head, AITG (with a request to upload this O.M on the institute's website)