

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI

No. 4/2-3(5)/2025-E-I

Date: 06.10.2025

OFFICE MEMORANDUM

Sub :- Result for the post of PAT-I and PAT-II

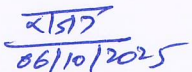
Ref: CSIR Letter dated 02.09.2024 No. 5-1(342)/2017-PD: *Guidelines for engagement of project personnel under various CSIR (incl. NMITLI, OLP, MLP, industry sponsored, Contract/ Consultancy / Sponsored projects etc) and non-CSIR funded projects - reg. (clause 4.4)*

Based on the recommendations of the Selection Committee & performance of candidates in Interview held in hybrid mode, the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement of PAT-I And PAT-II.

The details are as under: -

Name of Position: PAT-I And PAT-II
Project No.: GAP-3141, HCP-007 AND CLP-3465
Number of Position: 04

S.No.	Project No.	Name	Name of Position	Emolument(s)
1.	GAP-3141	Rakesh	PAT-I	₹25,000/-
2.	HCP-0007	Aswathy J	PAT-II	₹35,000/-
3.	HCP-0007	Abhishek	PAT-I	₹25,000/-
4.	CLP-3465	Dev Krishan Prajapat	PAT-I	₹25,000/-


06/10/2025
SECTION OFFICER