

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

List of Shortlisted candidates for the Post Code 3330 & Project No. GAP-6523

(Advt. No. PF-10/2025)

Following Candidates have been shortlisted for one position Sr. PAT/ PAT-II against the Post Code 3330 & Project No. GAP-6523 advertised vide Advt. No. PF-10/2025, for online interview (through MS Teams) as per the schedule given below:

S. No.	Application Number	Name of Candidate	Interview Date & Time
1.	1760008480	Babul Saini	15.10.2025 At 10.00 AM
2.	1759851390	Rajat Singh	

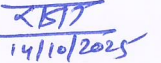
Criteria adopted for shortlisting:

B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.>= 60%.

Candidates must join the said meeting at least 15 minutes before through MS Teams Link. They will be admitted for interview according to their turn as per attached list of shortlisted candidates. The candidates have to wait for their turn, if all available candidates interviewed and no candidate available in MS Teams lobby, then selection committee meeting will be over.

Important Informations:

1. The shortlisted candidates have to appear for online interview as per the date and time given above.
2. The link is being sent to their email IDs.
3. The candidates have to join the interview with their application number followed by - and their full name. Example: If the application Number is **1234567890** and Name is **Ramesh Kumar**, then join with **1234567890 - Ramesh Kumar**.
4. The shortlisting has been done on the basis of information provided by the candidates in the online application. If any information found incorrect, the candidature will be rejected without further correspondence.


14/10/2025
Section officer