

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE,

PILANI (RAJ.)

Advt. No. PF-08/2025

Dated: 28.08.2025

Online Interview/In Person Interview

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

**Candidates are requested to read “[Instruction To Apply](#)” before apply.
Online applications are invited from candidates for the Engagement of Project Staff.**

Details of Vacancies & Eligibility

Post code	Project No. & Title	Area/Group	Name & Number of Positions	Essential Qualification as on the last date of receipt of online applications i.e. 07.09.2025	Age	Consolidated Emoluments Per month (fixed)
3315	GAP-3251 & Design and Fabrication of GaN Based Green Light Emitting Diode for Decoration and Other Applications	Semiconductor Process Technology Group (SPTG)	PAT-II & 01	B.E./ B.Tech/ M.Sc (Physics/ Applied Physics/ Applied Optics/ Optoelectronics/ Laser Physics/ Nano Technology/ E&C/ Electronics/ Radio Physics & Electronics) / Semiconductor Technology or equivalent. And 2-year R&D research experience in industry / R&D institutes. Preference will be given to NET/GATE qualified students.	35 Years	Rs. 35,000/- or Rs. 28,000/-
3316	GAP 6224 & Large Scale Deployment and Scaling up of Power Assistive Hybrid E-Trike for people with disability; PAH E-Trike Phase II	Societal Electronics Group (SEG)	Scientific Administrative Assistant / Field Worker & 01	Essential Qualification – Graduate Degree in Any Discipline Eligibility – specialization in Electronics / Electrical / Computer / Robotics / Mechanical / Physics / Computer Science / IT / AI / ML / Data Science etc.	50 Years	Rs. 18,000/-
3317	GAP6525 & Development of MEMS-based Piezoelectric Sensor for High SPL applications	Semiconductor Sensors and Microsystem Group (SSMG)	JRF/PAT-I & 01	B.E./B.Tech. (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics / Applied Physics/ Nanotechnology/ Electronics/Microelectronics/ VLSI Design/ Instrumentation) or equivalent OR M.Sc. (Electronics/Physics/ Applied Physics/Applied Electronics/E&I) or equivalent selected through a process described through any of the following: (a) Scholars who are selected through CSIR-UGC NET and GATE	35 Years	For JRF Rs. 37,000/- or For PAT-I Rs. 31,000/- or Rs. 25,000/-

				<p>(b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC, IISER etc. AND two years of research experience</p> <p>Desirable: M.E./M.Tech. (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/Microelectronics/ VLSI Design/ Instrumentation) or equivalent</p> <p>*Candidates with above qualification without GATE/CSIR-UGC NET or National Level Examination can also apply. They shall be eligible for the position of Project Associate-I (PAT-I).</p>		
3318	HCP-0047 & Phenome India-CSIR Health Cohort Knowledgebase (Cohort Enrolment and sample Collection)	Semiconductor Process Technology Group (SPTG)	PAT-I & 01	B. E., B. Tech. in engineering/biotechnology or M. Sc. or equivalent in natural Sciences.	35 Years	Rs. 31,000/- or Rs. 25,000/-
3319	MMP015201 (WP-2.5) & Development of Micro-Electro Mechanical Switches for Terahertz Technologies	Semiconductor Process Technology Group (SPTG)	PAT-I & 01	B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.	35 Years	Rs. 31,000/- or Rs. 25,000/-
3320	GAP-6523 & Multiport Switch with Tuneable Impedance Matching Network for a Single Broadband Antenna to Cover 2G, 3G, 4G and 5G Bands	Semiconductor Process Technology Group (SPTG)	Senior PAT/PAT-II & 01	<p>For Senior PAT</p> <p>B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.</p> <p>And 03 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p>Or M. Tech. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.</p> <p>Or Ph.D. in engineering/Science</p> <p>And 02 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and</p>	35 Years for Senior PAT-II or 40 years for Senior PAT as per CSIR rules	Rs. 42000/- for Senior PAT or Rs. 35000/- or Rs. 28000/- For PAT-II

				Scientific activities and services For PA-II B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent. And 02 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services.		
3321	GAP-3252 & Design, Fabrication and packaging of 650V pGaN E-mode HEMT for Power Switching Applications.	Semiconductor Process Technology Group (SPTG)	SRF & 01	Graduate degree/B.E/B.Tech/ Post graduate degree/ M.Sc. (VLSI, Electronics/ Physics/ Applied Physics/ Applied Electronics/ E & I) degree holders with two years research experience, with valid NET or GATE score. or M.E/ M.Tech in (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Semiconductor/ Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI Design) with valid NET or GATE score.	35 Years	Rs. 42,000/-
3322	GAP-6523 & Multiport Switch with Tuneable Impedance Matching Network for a Single Broadband Antenna to Cover 2G, 3G, 4G and 5G Bands	Semiconductor Process Technology Group (SPTG)	PAT-I & 01	B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.	35 Years	Rs. 31,000/- or Rs. 25,000/-
3323	GAP-3250 & Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle WP: 2.1 Development of wideband-gap based EV inverters, motor drive (400 V-1500 V) and Integrated Safety Control Framework WP-2.3: Integrated Safety Control Framework for High Voltage DC-DC Converters: Leveraging Monitoring, Predictive Maintenance, and Compliance with Industry Standards WP-4.2: Development of Modular Converter Architecture of High Frequency Inverter	Semiconductor sensors and Microsystem Group (SSMG)	JRF 05 & SRF 02 Note: 1. If JRF candidate is not found, then a Project Associate-I / Project Associate-II may be hired.	JRF: Graduate degree/BE/B.Tech/Post graduate degree /ME/M.Tech in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation/Computer Science or equivalent; selected through a process described through any one of the following: (a). Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. (b) The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. OR	35 Years	SRF: 42,000/- and JRF: Rs. 37,000/- or Project Associate – I: Rs. 30,000/- and Project Associate – II: Rs. 33,000/- as per ANRF/OM/ N-01/2024

	WP-4.3: Development of Modular Architecture of DC-DC Converters and Onboard Chargers with Condition Monitoring and Health Management			<p>SRF: JRF with 2 years of Research experience.</p> <p>OR</p> <p>Project Associate – I: B.E/B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation/Computer Science or equivalent.</p> <p>OR</p> <p>Project Associate – II: B.E/B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent/Computer Science AND 2 years' experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services.</p> <p>OR</p> <p>Master's/ Integrated Master's in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation Engineering/Computer Science.</p>		
3324	MMP- 015201 & Development of signal processing algorithms for FMCW RADAR	Advanced Information Technologies Group (AITG)	PAT-I/PAT-II & 01	<p>Essential Qualification: For PAT-I/PAT-II: B.E./ B.Tech.(CSE/ IT/ ECE/ Robotics/EE/ EEE/E&I/Instrumentation/Mechanical)/M.Sc(Electronics/Computer Science/IT) or equivalent with a degree in related areas.</p> <p>Additional Requirements For PAT-II: 02 years experience in Research and Development</p> <p>Desirable Experience: Working experience and domain knowledge of Embedded Systems, Microcontrollers, Signal Processing, AI, Machine learning, with working knowledge of C++/Python</p>	35 Years	For PAT-I Rs. 31,000/- or Rs. 25,000/- For PAT-II Rs. 35,000/- or Rs. 28,000/-

*Candidates with above qualification without experience can also apply they shall be eligible for the position of PAT-I

For PAT – I:

(i) ₹31,000/- + HRA to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments

and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹25,000/- + HRA for others who do not fall under (i) above.

* For PAT – II:

(i) ₹35,000/- + HRA to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments and their

Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹28,000/- + HRA for others who do not fall under (i) above

For AcSIR Ph.D. (Engineering/IDDP) Students

- 1. Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.***
- 2. Those who are having fellowship, they are not eligible to apply.***

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

For Recruitment queries: Sh. Rajan Tirkey Section Officer CSIR-CEERI, Pilani Email: recruitment.ceeri@csir.res.in Phone:01596-252275	For Technical queries: Sh. Sanjeev Kumar Advanced Information Technologies Group CSIR-CEERI, Pilani Email: sanjeevkumar.ceeri@csir.res.in Phone:01596-252282
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Other Conditions:

- Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ PAT-I/II/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ PAT-I/II etc. for a total period of 6 years or more are not eligible for these engagements. The candidates who have served for a period less than 6 years will have tenure up to remaining period till completion of Six years.
- Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate, Project Assistant, PAT-I/II, JRF/SRF etc. shall not exceed 6 years in any case. The total tenure of Six years shall

be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instit. of CSIR as RA, Project Assistant, PAT-I/II etc. or as any other designation of equal status.

3. All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.

4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Scientific Administrative Assistant/Project Assistant, PAT-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PWBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
7. The relaxation in the age limit for the candidates belonging to the category of SC/ST/PwBD/Women and the category of OBC shall be 05 years and 03 years respectively or as per Govt. of India instructions issued in this regard from time to time. (Vide CSIR OM No. 5-1(342)/2017-PD dated September 02,2024.)
- 8. Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:**
 - a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions :-
 - i. 10th Standard
 - ii. 12th Standard
 - iii. Diploma Degree
 - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
 - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DOB such as 10th Standard, DOB Certificate, etc.)
- c. Category certificate (in case of SC/ ST/ OBC/ PWBD/ EWS candidates)
- d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)
- e. Experience certificate (Mandatory for the posts where experience is essential qualification)
- f. Other certificates (If applicable)
9. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
10. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application,

the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.

11. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.
12. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
13. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.
14. The number of positions may be increased or decreased as per the requirements.
15. HRA will be admissible as per CSIR guidelines and subject to availability of funds in the project.
16. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the engagement of above Project Staff as amended from time to time.

The last date & time for applying applications is 07.09.2025 till 05:00 P.M. Application received after last date will not be entertained.

**Sd/-
SECTION OFFICER**