



Advertisement No. PF-10/2025 dated: 30.09.2025

Online Interview/In Person Interview

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

Candidates are requested to read “[Instruction To Apply](#)” before apply.

Online applications are invited from candidates for the Engagement of Project Staff.

Details of Vacancies & Eligibility

Post code	Project No. & Title	Area/Group	Name & Number of Positions	Essential Qualification as on the last date of receipt of online applications i.e. 09.10.2025	Age	Consolidated Emoluments Per month (fixed)
3328	MMP-055201 & Design and Development of Temperature Sensor for Deepsea Application (V2-WP2)	Semiconductor Device Area (SSMG)	PAT-II & 01 Note: If PAT-II not found than PAT-I may be hired.	M.Tech in Electronics Engineering, Electronics and Communication Engineering, Electronics and Instrumentation Engineering, Electrical and Electronics Engineering, Mechatronics, or equivalent. Note: if a suitable candidate for PAT-II is not found, PAT-I may be considered, with the following qualification. "B.Tech in Electronics Engineering or B.Tech Electronics and Communication Engineering, or B.Tech in Electronics and Instrumentation or B.Tech in Electronics and Electrical Engineering or M.Sc. in Electronics or Equivalent	35 Years	For PAT-I Rs. 31,000/- Or Rs. 25,000/- For PAT-II Rs. 35,000/- Or Rs. 28,000/-
3329	GAP-3343 & Design and development of hollow cathode assembly for the emission characterization and work function measurement of LaB6 emitter.	High Power Microwave Devices Group (HPMSG)	PAT-I & 01	B.E./B.Tech/ M.Sc. in Instrumentation/ Electronics & Instrumentation/ ECE/ Electronics/ Physics or equivalent. GATE/ NET preferred.	35 Years	Rs. 31,000/- As per guideline of ISRO
3330	GAP-6523 & Multiport Switch with Tuneable Impedance Matching Network for a Single Broadband Antenna to Cover 2G, 3G,	Semiconductor Process Technology Group (SPTG)	Sr. PAT/ PAT-II & 01	For Senior PAT B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.	PAT-II: 35 Years Sr. PAT: 40 Years	Sr. PAT: Rs. 42,000/- For PAT-II: Rs. 35,000/- Or Rs. 28,000/-

	4G and 5G Bands			<p>And 03 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p>Or M. Tech. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.</p> <p>Or Ph.D. in engineering/Science</p> <p>And 02 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p>For PA-II B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS/Physics) or equivalent.</p> <p>And 02 years' experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services.</p>		
3331	MMP-085203 & CSIR 4M mission mode project entitled, "Solar Panels, Electrical/ Electronics Applications & Air-Conditioner Wastes to Sustainable Materials."	High Power Microwave Devices Group (HPMSG)	PAT-I & 01 NOTE: The Joining Letter issued after the availability of fund.	B.Tech/B.E in Electronics and Communication Engineering/ Microwave M.Sc in Physics/ Electronics/ Microwave.	35 Years	Rs. 25,000/-
3332	MMP-085203 & CSIR 4M mission mode project entitled, "Solar Panels, Electrical/ Electronics Applications & Air-Conditioner Wastes to Sustainable Materials."	High Power Microwave Devices Group (HPMSG)	PAT-I & 01 NOTE: The Joining Letter issued after the availability of fund.	B.E/ B.Tech in Electronics/ Electrical Engineering/ Electrical & Electronics Engineering / Electronics & Communication Engineering/ Electronics & Instrumentation/ Control and Automation from a recognized university.	35 Years	Rs. 25,000/-
3333	MMP-015201(W.P-2.4) & CSIR DELTA Mission: (W.P-2.4) "Design and Development of GaN HEMT for THz RADAR and Communication	Semiconductor Process Technology Group (SPTG)	PAT-I/II & 01	For PAT-I M.Sc. (Electronics/Physics/ Applied Physics/ Applied Electronics/ E&I) or equivalent or B.E/B.Tech/M.E/ M.Tech in (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI Design/ Instrumentation) or equivalent. For PAT-II 02 Years experience in Research	35 Years	For PAT-I Rs. 31,000/- Or Rs. 25,000/- For PAT-II Rs. 35,000/- Or Rs. 28,000/-

				and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services with above mentioned qualifications. Desirable: Knowledge of semiconductor device design and simulation (compound semiconductor), semiconductor fabrication, GaN-HEMT/RF devices, GaN Power Amplifier, Synopsys/ ADS etc.		
3334	GAP-6107 & Dual Agricultural Land Utilization through Photovoltaic-based DC Microgrid.	Industrial Automation Group (IAG)	PAT-II & 01 Note: If PAT-II not found than PAT-I may be hired.	Project Associate-(PAT-II) Essential Qualification: B.E./ B.Tech./M.Sc. in Electronics & communication/Electronics/ Electrical and Electronics/ Electrical/Instrumentation/Physics/ Applied Physics/ Optoelectronics/ Nanotechnology/ Microelectronics/VLSI Design/ or Equivalent AND 02 Years' experience in research and development in industrial and academic institutes or science and technology organizations and scientific activities and services. <i>*Candidates with above qualification without experience can also apply. They shall be eligible for Project Associate-I (PAT-I) position.</i> Desirable: Working experience and domain knowledge of areas of power electronics or circuit design or renewable systems.	35 Years	For PAT-I Rs. 31,000/- Or Rs. 25,000/- For PAT-II Rs. 35,000/- Or Rs. 28,000/-
3335	MMP055201 & Design and Development of Pressure Sensor for Deepsea Application (V2WP1)	Semiconductor Device Area (SSMG)	PAT-II & 01 Note: If PAT-II not found than PAT-I may be hired.	"M.Tech in Electronics Engineering, Electronics and Communication Engineering, Electronics and Instrumentation Engineering, Electrical and Electronics Engineering, Mechatronics, or equivalent. Note: if suitable candidate for PAT-II is not found PAT-I may be considered, with following qualification. "B.Tech in Electronics Engineering or B.Tech Electronics and Communication Engineering, or B.Tech in Electronics and Instrumentation or B.Tech in Electronics and Electrical Engineering or M.Sc. in Electronics or Equivalent	35 Years	For PAT-I Rs. 31,000/- Or Rs. 25,000/- For PAT-II Rs. 35,000/- Or Rs. 28,000/-
3336	GAP-6525 & Development of MEMS-	Semiconductor Sensors and Microsystem	JRF & 01	B.E/ B.Tech (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/	35 Years	For JRF: Rs. 37,000/- For PAT-I:

	based Piezoelectric Sensor for High SPL applications	Group (SSMG)	Note: If JRF not found than PAT-I may be hired.	Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI Design/ Instrumentation) or equivalent. OR M.Sc. (Electronics/ Physics/ Applied Physics/ Applied Electronics / E&I) or equivalent selected through a process described through any of the following: (a) Scholars who are selected through CSIR-UGC NET and GATE (b) The selection process through National Level examinations conducted by Central Government Department and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO , MHRD, ICAR ICMR, IIT, IISER etc. AND two years of reseach experience. Desirable: M.E/ M.Tech (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI/ Design/ Instrumentation) or equivalent Candidate with above qualification without GATE/CSIR-UGC NET or National Level Examination can also apply. They shall be eligible for the position of Project Associate-I (PAT-I).		Rs. 25,000/-
3337	FTT010501 & Development of Pyroelectric Detector Array Technology for a Portable Spectrophotometer	Semiconductor Process Technology Group (SPTG)	PAT-II/PAT-I & 01 Note: If PAT-II not found than PAT-I may be hired.	Project Associate-(PAT-II) Essential Qualification: B.E./ B.Tech./M.Sc. in Electronics & communication/Electronics/ Electrical and Electronics/ Electrical/Instrumentation/Physics/ Applied Physics/ Optoelectronics/ Nanotechnology/ Microelectronics/VLSI Design/ or Equivalent AND 02 Years' experience in research and development in industrial and academic institutes or science and technology organizations and scientific activities and services. <i>*Candidates with above qualification without experience can also apply. They shall be eligible for Project Associate-I (PAT-I) position.</i>	35 Years	For PAT-I Rs. 31,000/- Or Rs. 25,000/- For PAT-II Rs. 35,000/- Or Rs. 28,000/-
3338	CLP-3363 & Development of Cathode Support-heater assembly	VEDG	Project Assistant-II & 01	B.Sc. (PCM)/ Diploma in Electronics and Communication/ Electronics/ Electrical or equivalent.	35 Years	Rs. 20,000/-

	for 8.0 kW S-band CW Magnetron					
3339	GAP-3251 & Design and Fabrication of GaN Based Green Light Emitting Diode for Decoration and Other Applications	Semiconductor Process Technology Group (SPTG)	PAT-I & 01	B.E./ B.Tech/ M.Sc. (Physics/ Applied Physics/ Applied Optics/ Optoelectronics/ Laser Physics/ Nano Technology/ E&C/ Electronics/ Radio Physics & Electronics) / Semiconductor Technology or equivalent. And Valid GATE/NET	35 Years	Rs. 31,000/- or Rs. 25,000/-
3340	MMP015201(WP:2.1.2) & Developing Expertise and Leveraging Terahertz Technology Advancements in CSIR (DELTA) (FPGA based chirp generator for FMCW RADAR)	Advance Information Technology Group (AITG)	PAT-I/II & 01 Note: If PAT-II not found than PAT-I may be hired.	B.E/ B.Tech/ M.Sc. (Physics/ Electronics/ Communication/ Instrumentation) or equivalent. And 02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services. Desirable: M.E/ M.Tech. (Electronics/ Nanotechnology/ Material Science/ Instrumentation / Microwave/ Electronics & Communication) or equivalent Skillset: FPGA based system design, VHDL/ Verilog, VLSI Design.	35 Years	For PAT-I: Rs. 31,000/- Or Rs. 25,000/- For PAT-II: Rs. 35,000/- Or Rs. 28,000/-
3341	GAP-6524 & Development of Microfluidic-based viscosity Measurement System for Adulteration in Edible Oils	Semiconductor Sensor and Microsystems Group (SSMG)	JRF & 01 Note: If JRF not found than PAT-I may be hired.	JRF: Post Graduate Degree in Electronics/ Applied Electronics/ Nanoelectronics/ E&I or equivalent. Or B.E/ B.Tech/ ME/ M.Tech/ in EEE/ Electronics/ ECE/ Electronics & Instrumentation/ Microelectronics or equivalent; selected through a process described through any of the following: (a) Scholars who are selected through National Eligibility Tests-CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. (b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. OR PAT-I: B.Tech in Electronics Engineering or B.Tech Electronics and Communication Engineering or B.Tech in Electronics and	35 Years	JRF: Rs. 37,000/- PAT-I: Rs. 25,000/-

				Instrumentation or B.Tech in Electronics and Electrical Engineering or M.Sc in Electronics or equivalent.		
3342	FTT010501 & Development of Pyroelectric Detector Array Technology for a Portable Spectrophotometer	Semiconductor Process Technology Group (SPTG)	Project Assistant-II & 02	Project Assistant-II Essential Qualification: B. Sc. / 3 Years Diploma in Electronics / Electrical/Mechatronics / Computer Science / IT or equivalent Desirable Qualification: • Knowledge of PCB design, system and interfacing integration, data acquisition and hardware debugging.	35 Years	Project Assistant-II: Rs. 20,000/-

*Candidates with above qualification without experience can also apply they shall be eligible for the position of PAT-I

For PAT – I:

- (i) ₹31,000/- + HRA to Scholars who are selected through-
- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
 - A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.
- (ii) ₹25,000/- + HRA for others who do not fall under (i) above.

* For PAT – II:

- (i) ₹35,000/- + HRA to Scholars who are selected through-
- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
 - A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.
- (ii) ₹28,000/- + HRA for others who do not fall under (i) above

For AcSIR Ph.D. (Engineering/IDDP) Students

- Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.
- Those who are having fellowship, they are not eligible to apply.

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

For Recruitment queries:

Sh. Rajan Tirkey
Section Officer
CSIR-CEERI, Pilani
Email: recruitment.ceeri@csir.res.in
Phone:01596-252275

For Technical queries:

Sh. Sanjeev Kumar
Advanced Information Technologies Group
CSIR-CEERI, Pilani
Email: sanjeevkumar.ceeri@csir.res.in
Phone:01596-252282

How to Apply:

Read the instruction given on the CSIR-CEERI Website on Project Staff Recruitment Page.

Or

Given the link in blue before the table in this advertisement.

Other Conditions:

1. Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ PAT-I/II/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ PAT-I/II etc. for a total period of 6 years or more are not eligible for these engagements. The candidates who have served for a period less than 6 years will have tenure up to remaining period till completion of Six years.
2. Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate, Project Assistant, PAT-I/II, JRF/SRF etc. shall not exceed 6 years in any case. The total tenure of Six years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as RA, Project Assistant, PAT-I/II etc. or as any other designation of equal status.
3. **All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**
4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Scientific Administrative Assistant/Project Assistant, PAT-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PWBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
7. The relaxation in the age limit for the candidates belonging to the category of SC/ST/PwBD/Women and the category of OBC shall be 05 years and 03 years respectively or as per Govt. of India instructions issued in this regard from time of time. (CSIR OM No. 5-1(342)/2017-PD dated September 02,2024.)
8. **Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:**
 - a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
 - i. 10th Standard
 - ii. 12th Standard
 - iii. Diploma Degree
 - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
 - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DOB such as 10thStandard, DOB Certificate, etc.)

- c. Category certificate (in case of SC/ ST/ OBC/ PWBD/ EWS candidates)
- d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)
- e. Experience certificate (Mandatory for the posts where experience is essential qualification)
- f. Other certificates (If applicable)
9. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
10. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
11. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.
12. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
13. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.
14. The number of positions may be increased or decreased as per the requirements.
15. HRA will be admissible as per CSIR guidelines and subject to availability of funds in the project.
16. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the engagement of above Project Staff as amended from time to time.

The last date & time for applying applications is 09.10.2025 till 05:00 PM Application received after last date will not be entertained.

Sd/
Section Officer