

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

List of Shortlisted candidates for the Post Code 3299 & Project No. GAP-3250

(Advt. No. PF-06/2025)

Following Candidates have been shortlisted for two position of JRF against the Post Code 3299 & Project No. GAP-3250 advertised vide Advt. No. PF-06/2025, for online interview (through MS Teams) as per the schedule given below:

Post Code :	3299	ShortListing Criteria :	Interview Date & Time
S.No.	Application Number	Name of Candidate	12.08.2025 at 12.30 PM
1	1753765981	Faisal Ahmad Chaudhary	
2	1753699911	Sourik Ganguly	
3	1753926311	Abhijeet singh	
4	1753944482	BETHU JAYANTH	
5	1753880930	Raman Jha	
6	1753875698	Ehraz Ashraf	
7	1753781008	AJAY SINGH	
8	1753986448	Karishma Goswami	
9	1753966825	Monika Chaparia	
10	1753965550	IFTEKHAR AHMAD	
11	1753938087	AMAN DADSENA	
12	1753895112	ASWIN VYSAKH P	
13	1753815819	Annu Chauhan	
14	1753805382	Rohit Pratap Nair	
15	1753783799	Gopal Singh	
16	1753720166	SOBHA RANI A	
17	1753716790	Sagufta Simran	
18	1753550994	Rajveer kajla	

Criteria adopted for shortlisting:

JRF:

Graduate degree / B.E. / B.Tech/ M.Sc. / Post graduate degree /M.E. / M.Tech in (Physics / Engineering Physics / Nano-Technology/Electronics/ Electrical Engineering / Electrical & Electronics Engineering / Electronics & Communication Engineering / Electronics & Instrumentation / Electrical / Mechatronics / Instrumentation & Control) or equivalent; selected through a process described any one of the following:

(a)Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.

(b)The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER

PAT-I:

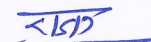
80% and B.E. /B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent.

PAT – II:

80% and B.E. / B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent **AND** 2 years' experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services.

OR

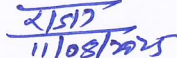
80% Masters/Integrated Masters in Electronics/Electrical Engineering/Electrical & Electronics



Candidates must join the said meeting at least 15 minutes before through MS Teams Link. They will be admitted for interview according to their turn as per attached list of shortlisted candidates. The candidates have to wait for their turn, if all available candidates interviewed and no candidate available in MS Teams lobby, then selection committee meeting will be over.

Important Informations:

1. The shortlisted candidates have to appear for online interview as per the date and time given above.
2. The link is being sent to their email IDs.
3. The candidates have to join the interview with their application number followed by - and their full name. Example: If the application Number is **1234567890** and Name is **Ramesh Kumar**, then join with **1234567890 - Ramesh Kumar**.
4. The shortlisting has been done on the basis of information provided by the candidates in the online application. If any information found incorrect, the candidature will be rejected without further correspondence.


11/08/2025
Section officer