

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE,  
PILANI

सं. 4/2-3(4)/2022-स्था. I

Date: 01.07.2025

**OFFICE MEMORANDUM**

**Result for the post of pat-I advertised vide Advt. No. PF- 04/2025.**

Based on the recommendations of the Selection Committee & performance of candidates in Online Interview (held through MS Teams), the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement JRF/PAT-II/I that are empanelled in order of merit.

The details are as under: -

**POST CODE- 3278**

Name of Position: JRF/PAT-II/I

Project No.: GAP- 3344

Number of Position: 01

Order of Merit	Application No.	Name of Candidate Included in Panel	Name of Position	Emolument(s)
1.	1748709804	Prafful S Navalagunda	JRF/PAT-II/I	JRF: ₹ 37000/- PAT-I: ₹ 25,000/- PAT-II: ₹ 35000/-

*2/5/25*  
*09/07/2025*  
**SECTION OFFICER**

**Note:-**

The above panels will remain valid for a period of One Year from the date of issue of result or up to the duration of the project, whichever is earlier. The above panels does not confer any right on the empanelled candidates for engagement as Project Staff positions. The engagement to the Project Staff positions from the above panels will be made subject to the availability of vacancies in the Projects.