

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE

No. 4/2-2(10)/2021-E.I

Date: 14.12.2024

OFFICE MEMORANDUM

Sub: Schedule of Written Examination in respect of CSIR-CEERI Advertisement No. 02/2023 for the post of Technical Officer – reg.

Written Examination for the posts of Technical Officer against the Advt. No. 02/2023 will be conducted in the Institute on 15th December, 2024. The Competent Authority has accorded his approval to conduct the written examination.

Schedule of Written Examination is as follows:

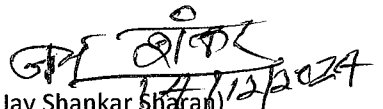
Reporting Time: 09:30am

Subject	Time	Venue
Paper-I Mental Ability Test	10:00 am -11:00 am	CSIR-CEERI
Paper-II General Awareness & English Language	11:15am – 11.45am	CSIR-CEERI
Paper-III Electronics / MBA	12:00 pm -01:30pm	CSIR-CEERI

The qualifying criteria for written test for Paper-I shall be 40% for UR/OBC/EWS and 35% for SC/ST/PwBD candidates.

Admit Card for the written examination will be issued to qualified in trade test on reporting to written examination at venue.

The candidates are also advised to visit the CSIR-CEERI website www.ceeri.res.in regularly for latest updates.


(Jay Shankar Sharan)
Controller of Administration

Copy to:

1. Head (NMC) - with the request to upload the OM in CSIR-CEERI website.
2. Notice Board.