

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI

सं. 4/2-3(4)/2022-स्था. I

Date: 07.11.2024

OFFICE MEMORANDUM

Result for the post of Junior Research Fellow advertised vide Advt. No. PF- 06/2024.

Based on the recommendations of the Selection Committee & performance of candidates in Online Interview (held through MS Teams), the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement of Junior Research Fellow that are empanelled in order of merit.

The details are as under: -

POST CODE- 3255

Name of Position: Junior Research Fellow

Project No.: GAP-6522

Number of Position: 02

Order of Merit	Application No.	Name of Candidate Included in Panel	Emolument(s)
1.	1729505694	Pallavi Prajapati	₹37,000/- p.m. + HRA as per rules
2.	1729676821	Aditi Joshi	₹37,000/- p.m. + HRA as per rules
3.	1729662700	Sourabh Bedwal	₹37,000/- p.m. + HRA as per rules
4.	1729666743	Ruchi Dang	₹37,000/- p.m. + HRA as per rules
5.	1729091675	Gyanendra Vimal	₹37,000/- p.m. + HRA as per rules

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07/11/2024

SECTION OFFICER

Note:-

The above panels will remain valid for a period of One Year from the date of issue of result or up to the duration of the project, whichever is earlier. The above panels does not confer any right on the empanelled candidates for engagement as Project Staff positions. The engagement to the Project Staff positions from the above panels will be made subject to the availability of vacancies in the Projects.