

CSIR-CENTRAL ELECTRONICS ENGINEERING CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH
INSTITUTE, PILANI

सं. 4/2-3(4)/2022-स्था. I

Date: 06.11.2024

OFFICE MEMORANDUM

Result for the post of Project Associate-I advertised vide Advt. No. PF- 05/2024.

Based on the recommendations of the Selection Committee & performance of candidates in Online Interview (held through MS Teams), the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement of Project Associate-I that are empanelled in order of merit.

The details are as under: -

POST CODE- 3227

Name of Position: Project Associate-I
Project No.: HCP-0042(WP-5.3)
Number of Position: 01

Order of Merit	Application No.	Name of Candidate Included in Panel	Emolument(s)
1.	1724305771	Pankaj Singh	₹ 25,000/- p.m. + HRA as per rules
2.	1723536916	Gaurav Kumar Singh	₹ 25,000/- p.m. + HRA as per rules

R. S. S.
06/11/2024

SECTION OFFICER

Note:-

The above panels will remain valid for a period of One Year from the date of issue of result or up to the duration of the project, whichever is earlier. The above panels does not confer any right on the empanelled candidates for engagement as Project Staff positions. The engagement to the Project Staff positions from the above panels will be made subject to the availability of vacancies in the Projects. CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI