

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

Advt. No. PF-06/2024

Dated: 08.10.2024

“Online Interview”

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

Online applications are invited from candidates for the Engagement of Project Staff.

Details of Vacancies & Eligibility

Post code	Project No. & Title	Area/Group	Name & Number of Positions	Essential Qualification as on the last date of receipt of online applications i.e. 15.10.2024	Age	Consolidated Emoluments Per month (fixed)
3236	HCP-0101 & JIGYASA	PME	PAT-II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.Tech./ B.E. (Computer Science/ IT/ Electronics/ Masters in Computer Science) or equivalent AND 2 years of experience. Or Preference: 1) Experience in Programming, Animation, Database creation ARVR, Front End Design, Microcontroller based App Development and related virtual/ algorithm development. 2) M.Tech in relevant stream or GATE/NET	35 Years	For PAT-II *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules For PAT-I ₹31,000/- + HRA as per rules OR ₹25,000/- + HRA as per rules
3237	IHP202405 & Design and Development of palladium based micro-thermal hydrogen sensor system for leak detection	Semiconductor Sensors and Microsystems Group (SSMG)	PAT-I & 01	B.Tech. (Electronics Engineering/ Electronics and Communication Engineering/ Electronics and Instrumentation/ Electronics and Electrical Engineering) or equivalent OR M. Sc (Electronics) or Equivalent	35 Years	₹31,000/- + HRA as per rules OR ₹25,000/- + HRA as per rules
3238	HCP-36 & Development of LTCC based accelerometer using PZT and lead free (BCZT) piezoceramics (WP under CSIR- Mission Mode Project entitled “Aerospace Materials and Technologies”)	Semiconductor Process Technology Group	PAT-I & 01	B.E./B.Tech/M.Sc (Electronics/ECE/ Electronics & Electrical Engineering/ Electronics & Instrumentation, Physics) or equivalent.	35 Years	₹31,000/- + HRA as per rules OR ₹25,000/- + HRA as per rules

****Note: Candidates without 02 year’s experience shall apply for PAT-II, they will be considered for the position of PAT-I**

3239	GAP-3245 & Indigenous development of 650 V GaN Power transistors on Si	Semiconductor Devices Design Group	PAT-I & 01	B.E./B.Tech./ M.Sc. (ECE/Computer Science/IT/Electrical/ Electronics) or equivalent	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3240	GAP-3247 MEMS Based Tunable Film Bulk Acoustic Resonators with Magneto-strictive/ Piezoelectric Bilayer Composite Thin Films	Semiconductor Sensors and Microsystems Group (SSMG)	PAT- II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech./M.Sc. (Physics/ Electronics/ Communication/ Instrumentation or equivalent) AND 02 years experience. OR M.Tech/ M.E. (Electronics/ Nanotechnology/ Material Science/ Instrumentation/ RF/ Microwave/ Electronics & Communication) or equivalent.	35 Years	<u>For PAT-II</u> *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3241	HCP-0042 WP 1.4 CSIR Mission Mode Project on 'AI- enabled Technologies & Systems (AITS)'	High Power Microwave System Group	PAT-II /**PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech/M.Sc (ECE/Computer Science/ IT/ Electrical/ Electronics) or equivalent OR B.E./ B.Tech/M.Sc (ECE/Computer Science/ IT/ Electrical/ Electronics) or equivalent AND 02 years of experience in R&D or Industry. Desirable: Working experience and domain knowledge of areas of AI algorithm development of circuit design.	35 Years	<u>For PAT-II</u> *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3242	GAP-6108 Development of Reconfigurable Ultra-compact Onboard Chargers for 400/800-V Battery Architectures	Societal Electronics Group	PAT-II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./B.Tech/M.Sc (ECE/EEE/EE/ Instrumentation/ Electrical/ Electronics/ Power Engineering/ Renewable Energy/ Power Electronics) or equivalent AND 02 years experience in research and development in industry and academic institutions or science and technology organizations. Desirable: Working experience and domain knowledge of power electronics or circuit design areas for EV chargers.	35 Years	<u>For PAT-II</u> *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3243	MMP015201 (WP:2.1.2) Developing Expertise and Leveraging Terahertz Technology Advancements in CSIR (DELTA) (FPGA based chirp generator for FMCW RADAR)	Advanced Information Technology Group (AITG)	PAT-II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./B.Tech./M.Sc. (Physics/Electronics/ Communication/Instrumentation) or equivalent OR B.E./B.Tech./M.Sc. (Physics/Electronics/ Communication/Instrumentation) or equivalent AND 02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services. Desirable: Working experience in VLSI Design, VHDL, FPGA.	35 Years	<u>For PAT-II</u> *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules

****Note: Candidates without 02 year's experience shall apply for PAT-II, they will be considered for the position of PAT-I**

3244	MMP015201 (WP-2.5) Development of Micro-Electro Mechanical Switches for Terahertz Technologies	Semiconductor Process Technology Group	PAT-I & 01	B.E./ B.Tech./ M.Sc. (Electronics/ECE/EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/ Mechatronics/ MEMS/Physics) or equivalent. Desirable: Knowledge Of RF design, HFSS, ADS etc.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3245	MMP015201 (WP-2.5) Development of Micro-Electro Mechanical Switches for Terahertz Technologies	Semiconductor Process Technology Group(SPTG)	PAT-II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech./ M.Sc. (Electronics/ECE/EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/ Mechatronics/ MEMS/Physics) AND 02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services. OR M.Tech. (Electronics/ Instrumentation/ Microwave/ Mechatronics/ MEMS/ Material Science/ EEE/ECE/ Physics) or equivalent. Desirable: Knowledge of RF design, HFSS, ADS etc.	35 Years	*₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3246	FIR010305 Development of Field-Emitter based Next Generation X-Ray Tube Technology for Medical and Industrial Applications	High Power Microwave System Group	PAT-I & 01	B.E./ B.Tech./ M.Sc. (ECE/ Electronics/ Physics/Nanotechnology) or equivalent. Preference: Working experience in automation using PLC, HMI and SCADA would be preferred.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3247	MMP015202 Manufacturing of Machines, Devices and Assembly Lines - MMP015202 (WP- A.6 IoT Based Transformer Health Monitoring System)	CSIR-CEERI Jaipur Campus	PAT-I & 01	B.E./ B.Tech. (Electrical Engineering/ Electrical and Electronics Engineering/ Electronics Engineering/ Instrumentation Engineering) or equivalent. Desirable: Knowledge in PCB Design, Microcontroller Programming, IoT Systems. Valid NET/GATE Score would be preferred.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules

****Note: Candidates without 02 year's experience shall apply for PAT-II, they will be considered for the position of PAT-I**

3248	MMP015201 WP-2.2 Design and Development of Un-cooled Compact Pyroelectric based THz Detector. Developing and Leveraging Terahertz Technology Advancements in CSIR (DELTA) Mission	Semiconductor Process Technology Group	PAT-II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech/M.Sc (Electronics & communication/Electronics/ Electrical and Electronics/ Electrical/ Instrumentation/ Physics/ Applied Physics/ Optoelectronics/ Nanotechnology/ Microelectronics/ VLSI Design) or equivalent. AND 02 Years experience in research and development in industrial and academic institutes or science and technology organization and scientific activities and services. Knowledge of semiconductor device design and simulation will be given preference. OR M.E./ M.Tech./ Inetegrated M.E. (Electronics & Communication/Electronics/ Electrical and Electronics/ Electrical/ Instrumentation/ Physics/ Applied Physics/ Optoelectronics/ Nanotechnology/ Microelectronics/ VLSI Design) or equivalent.	35 Years	*₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3249	MMP015202 (WP:C.2) IoT Enabled Portable 3D Endoscope for Minimal Invasive Surgeries (MIS)	Advance Information Technology Group (AITG)	PAT -II/ ** PAT-I & 02 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech/M.Sc (Electronics/ Instrumentation & Control Systems/ Electrical Engg./ EEE/CS/CE) or equivalent. AND 02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services. OR M.E./ M.Tech. (Electronics/ Nanotechnology/ Material Science/ Instrumentation/ Microwave/ Electronics & Communication) or equivalent. Skillset: (i) Microcontroller/ FPGA/ Processor-based hardware design; and (ii) AI/ML- based application design.	35 Years	<u>For PAT-II</u> *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3250	GAP-3140 Affordabled IoT-enabled Water Service Delivery Measurement and Monitoring Sensing System	CSIR-CEERI, Jaipur Campus	PAT-I & 02	B.E./ B.Tech (Electronics & Communication/ Electrical & Electronics/ Electronics & Instrumentation/ Computer Science/ Information & Technology or Power Electronics) or equivalent. OR M.Sc. (Electronics) or equivalent.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules

*****Note: Candidates without 02 year's exeperience shall apply for PAT-II, they will be considered for the position of PAT-I***

3251	GAP-6520 Design and Development of Piezoelectric MEMS sensor for low-SPL applications	Semiconductor Sensors and Microsystem Group	PAT-I & 01	B.E./ B.Tech. (EEE/ Electronics/EE/ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied Physics/ Nanotechnology/ VLSI Design/ Instrumentation) or equivalent OR M.Sc (Electronics/ Physics/ Applied Electronics/E&I) or equivalent	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
			JRF & 01 <i>(Note: If suitable candidate is not found for JRF position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech. (EEE/ Electronics/EE/ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied Physics/ Nanotechnology/ VLSI Design/ Instrumentation) or equivalent OR M.Sc (Electronics/ Physics/ Applied Electronics/E&I) or equivalent Selected through a process described through any of the following: (a) Scholars who are selected through CSIR- UGC NET and GATE (b) The selection process through National Level Examinations conducted by Central Government Departments and their Agencies and institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc. Desirable: M.E./M.Tech (EEE/Electronics/ EE/ECE/Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/ VLSI Design/ Instrumentation) or equivalent.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3252	HCP- 0057 (WP3E1) Region Specific Smart Agrotechnologies For Enhancing Soil And Plant Health	Industrial Automation Group	PAT-II/ **PAT-I & 01 <i>(Note: If suitable candidate is not found for PAT-II position then the candidate for PAT-I position will be considered)</i>	B.E./ B.Tech./M.Sc (Physics/ Agriculture/ Agriculture Technology/ E&C/ Electronics/ Electronics & Instrumentation) or equivalent. And 02 years experience	35 Years	<u>For PAT-II</u> *₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules <u>For PAT-I</u> #₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3253	GAP- 3340 A portable image-guided robotic cold plasma system for localized and targeted therapy of oral cancers	Societal Electronics Group	Project Technical Support-III & 02	First class PG/ M.Sc/ M. Tech. in (Physics/ Electronics/ Electrical/ Power Electronics/ Instrumentation/ Biophysics/Biochemical/Biochemistry/ Biological Sciences) Desirable: Exposure to design and development of gas discharge/ non-thermal plasma/ plasma devices and technologies. (Candidate having experience in design and development works related to gas discharge/ non- thermal plasma/ plasma devices and technologies, would be given preferences.)	35 Years	₹28,000/- per month + HRA as per rules

****Note: Candidates without 02 year's experience shall apply for PAT-II, they will be considered for the position of PAT-I**

3254	GAP-3332 Mercury free Far UV-C (222nm) Excimer Radiation Source for inactivation of Pathogens	Societal Electronics Group	PAT-I & 01	B.E./ B.Tech./ M.Sc (Electrical/ Electronics/ Eletronics & Communication/ Instrumetation/ Physics) or equivalent. Desirable: Candidate having experience in design and development works related to Plasma Assisted Devices and Technologies, would be given preferences.	35 Years	₹31,000/- + HRA as per rules OR ₹25,000/- + HRA as per rules
3255	GAP-6522 Development of Pyroelectric Infrared Sensors for Industrial Flame Detectors	Semiconductor Process Technology Group	JRF & 02	M.Sc (Electronics/ Physics/ Applied Electronics/ E&I) or equivalent OR B.E./ B.Tech./ M.E./ M.Tech. (EEE/ Electronics/EE/ECE/Electronics & Instrumentation/ Optoelectronics/ Microelectronics/ Applied Physics/ Nanotechnology/Electronics/ VLSI Design/ Instrumenatation) or equivalent + selected through a process described through any one of the following: (a) Scholars who are selected through National Eligibility Tests- CSIR- UGC NET including lectureship(Assistant Professorship) and GATE. (b) The selection process through National Level Examinations conducted by Central Government Departments and their Agencies and institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISC, IISER, NISER etc.	35 Years	₹37,000/- + HRA as per rules
3256	CLP- 3162 Delvelopment of Portable IoT Enabled Waste Water Quality Estimation System	Advanced Infiramation Tachnologies Group	PAT-I	SSC/10 th plus ITI	35 Years	18,000/- + HRA as per rules
3257	GAP-3343 Design and development of sealed and plasma assisted vacuum tubes for mm-wave absorption	High Power Microwave System Group	JRF & 01	M.Sc /B.E./ B.Tech/BVDC/Pharm	35 Years	₹37,000/- + HRA as per rules

****Note: Candidates without 02 year's exeperience shall apply for PAT-II, they will be considered for the position of PAT-I**

For PAT-I:

- (i) ₹31,000/- + HRA to Scholars who are selected through-
 - (a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
 - (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.
- (ii) ₹25,000/- + HRA for others who do not fall under (i) above.

* For PAT-II:

- (i) ₹35,000/- + HRA to Scholars who are selected through-
 - (a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
 - (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.
- (ii) ₹28,000/- + HRA for others who do not fall under (i) above.

For AcSIR Ph.D. (Engineering/IDDP) Students

1. *Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.*
2. *Those who are having fellowship, they are not eligible to apply.*

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

READ **INSTRUCTION TO APPLY** BEFORE APPLYING OTHERWISE YOU WILL FACE SOME PROBLEM DURING APPLY.

For Recruitment queries: Sh. Rajan Tirkey Section Officer CSIR-CEERI, Pilani Email: recruitment@ceeri.res.in Phone:01596-252275	For Technical queries: Sh. Sanjeev Kumar Advanced Information Technologies Group CSIR-CEERI, Pilani Email: sanjeevkumar@ceeri.res.in Phone:01596-252282
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Other Conditions:

1. Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ Project Fellow/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ Project Associate-I/II etc. for a total period of 6 years or more are not eligible for these engagements. The candidates who have served for a period less than 6 years will have tenure up to remaining period till completion of six years.
2. Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research PAT- I, Project Assistant, PAT-I/II, JRF/SRF etc. shall not exceed 6 years in any case. The total tenure of Six years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as RA, Project Assistant, PAT-I/II etc. or as any other designation of equal status.
3. **All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**
4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Project Assistant, PAT-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.

6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PwBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
7. The relaxation in the age limit for the candidate belonging to the category of SC/ST/PwBD/ Women and the category of OBC shall be 05 years and 03 years respectively or as per Govt. of India instructions issued in this regard from time to time

8. Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:

- a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
 - i. 10th Standard
 - ii. 12th Standard
 - iii. Diploma Degree
 - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
 - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DoB such as 10th Standard, DoB Certificate, etc.)
 - c. Category certificate (in case of SC/ ST/ OBC/ PwBD/ EWS candidates)
 - d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)
 - e. Experience certificate (Mandatory for the posts where experience is essential qualification)
 - f. Other certificates (If applicable)
9. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
 10. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
 11. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.
 12. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
 13. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.

14. The number of positions may be increased or decreased as per the requirements.

15. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the engagement of above Project Staff as amended from time to time.

The last date & time for applying applications is 23.10.2024 till 05:00 P.M. Application received after last date will not be entertained.

SECTION OFFICER