

**CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)**

**Advt. No. PF-04/2024**

**Dated: 11.06.2024**

**“Online Interview”**

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

**Online applications are invited from candidates for the Engagement of Project Staff.**

**Details of Vacancies & Eligibility**

<b>Post code</b>	<b>Project No. &amp; Title</b>	<b>Area/Group</b>	<b>Name &amp; Number of Positions</b>	<b>Essential Qualification as on the last date of receipt of online applications i.e. 24.06.2024</b>	<b>Age</b>	<b>Consolidated Emoluments Per month (fixed)</b>
3195	HCP-0036 Development of LTCC based accelerometer using PZT and BCZT piezoceramics	Semiconductor Process Technology Group (SPTG)	Project Associate-I & 01	B.E./B.Tech./M.Sc. [Electronics/ ECE/Electronics and Electrical Engineering(EEE)/ Electronics and Instrumentation] or equivalent	35 Years	₹31,000/- + HRA as per rules <b>OR</b> ₹25,000/- + HRA as per rules
3196	HCP-0057 (WP3E1) Region Specific Smart Agrotechnologies for enhancing soil and plant health	Semiconductor Process Technology Group (SPTG)	Project Associate-II & 01	B.E./ B.Tech./ M.Sc. (Physics/ Agriculture/Agriculture Technology/ E&C/Electronics/ Electronics & Instrumentation) or equivalent  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services	35 Years	*₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules
3197	CLP-3163 Development of PV based solar cooking system for rural application	CSIR-CEERI Jaipur Campus	Project Assistant & 01	03 years Diploma (Electrical Engineering/ Electrical and Electronics Engineering/ Electronic Engineering/ Instrumentation Engineering)  <b>Desirable</b> : Knowledge in power electronic circuits, pcb design	50 Years	**₹20,000/- + HRA as per rules
3198	IHP202402 Microwave Assisted Pyrolysis system	High Power Microwave Devices Group (HPMDG)	Project Associate-I & 01	B.E./ B.Tech. in Electronics and Communication Engineering/ Microwave/Instrumentation/ Nanomaterial Science or equivalent <b>OR</b> M.Sc. in Physics/Electronics/ Microwave/Nanomaterial Science or equivalent	35 Years	₹31,000/- + HRA as per rules <b>OR</b> ₹25,000/- + HRA as per rules

3199	IHP202403 Design and Development of microwave based grain moisture meter	High Frequency Devices and Systems Group (HFDSG)	Project Associate-I & 01	B.E./ B.Tech. in Electronics and Communication Engineering/ Computer Science and Engineering/ Electronics and Electrical Engineering or equivalent <b>OR</b> M.Sc. In Electronics/Computer Science or equivalent	35 Years	₹31,000/- + HRA as per rules <b>OR</b> ₹25,000/- + HRA as per rules
3200	GAP-3245 Indigenous development of 650 V GaN power transistors on Si	Semiconductor Devices Design Group (SDDG)	Senior Research Fellow & 01	M.E./M.Tech. (Microelectronics/ Advanced semiconductor Electronics/ Electronics Design and Technology/ECE/VLSI) selected through a process described through any of the following: (a) Scholars who are selected through CSIR-UGC NET and GATE (b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC, IISER etc.  <b>AND</b>  02 years of research experience	35 Years	₹42,000/- + HRA as per rules
3201	FTT010501 Development of Pyroelectric detector array technology for a portable spectrophotometer	Semiconductor Process Technology Group (SPTG)	Project Associate-II/ Project Associate-I & 02  <i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i>	B.E./B.Tech./M.Sc. In Electronics & Communication/ Electronics/ Electrical and Electronics/ Electrical/ Instrumentation/ Physics/ Applied Physics/ Optoelectronics/ Nanotechnology /Microelectronics/VLSI Design or equivalent  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> ₹35,000/- + HRA as per rules <b>OR</b> ₹28,000/- + HRA as per rules  <b>For PA-I</b> ₹31,000/- + HRA as per rules <b>OR</b> ₹25,000/- + HRA as per rules
3202	FTT010501 Development of Pyroelectric detector array technology for a portable	Semiconductor Process Technology Group (SPTG)	Project Assistant & 02	B.Sc. (PCM) / 03 years Diploma in Electronics/Electrical/ Mechatronics/Computer Science/IT or equivalent	50 Years	**₹20,000/- + HRA as per rules

	spectrophotometer			<b>Desirable</b> : Knowledge of PCB design, system and interfacing integration, data acquisition and hardware debugging.		
3203	GAP-6520 Design and development of piezoelectric MEMS sensor for low-SPL applications	Semiconductor Sensors and Microsystem Group (SSMG)	Junior Research Fellow/Project Associate-I & 01	B.E./B.Tech. (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics / Applied Physics/ Nanotechnology/ Electronics Microelectronics/ VLSI Design/ Instrumentation) or equivalent <b>OR</b> M.Sc. (Electronics/Physics/ Applied Physics/Applied Electronics/E&I) or equivalent selected through a process described through any of the following: (a) Scholars who are selected through CSIR-UGC NET and GATE (b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER etc. AND two years of research experience  <b>Desirable</b> : M.E./M.Tech. (EEE/ Electronics/ EE/ ECE/ Electronics & Instrumentation/ Optoelectronics/ Microelectronics/Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI Design/ Instrumentation) or equivalent	35 Years	₹31,000/- + HRA as per rules <b>OR</b> ₹25,000/- + HRA as per rules
3204	HCP-101 JIGYASA Virtual Labs.	Project Management & Evaluation Unit (PME)	Project Associate-I & 01	B.E./B.Tech. (Computer Science/ IT/Electronics) or equivalent <b>OR</b> M.Sc.(Computer Science/ Electronics) or equivalent  <b>Preferable:</b> Experience in Programming, Animation, Database creation ARVR, Front End Design, Microcontroller based App Development and related virtual/algorithm development	35 Years	₹31,000/- + HRA as per rules <b>OR</b> ₹25,000/- + HRA as per rules
3205	HCP-101 JIGYASA Virtual Labs.	PME	Project Associate-II & 01  <i>(Note: If suitable candidate is not found for PA-II position then</i>	B.E./B.Tech. (Computer Science/ IT/Electronics) or equivalent <b>OR</b> M.Sc.(Computer Science/ Electronics) or equivalent  <b>AND</b>	35 Years	*₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules

			<p><b>suitable candidate from Post Code 3204 will be considered for the position of PA-I)</b></p>	<p>02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p><b>Desirable:</b> M.E./M.Tech. in Computer Science/Electronics</p> <p><b>Preferable:</b> Experience in Programming, Animation, Database creation ARVR, Front End Desing, Microcontroller based App, Development and related virtual/algorithm development</p>		
3206	<p>HCP-007 (WP-1)</p> <p>Development of microwave assisted essential oil extraction system</p>	<p>High Frequency Devices and Systems Group (HFDSG)</p>	<p>Project Associate-II/ Project Associate-I &amp; 01</p> <p><b>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</b></p>	<p>B.E./B.Tech. (Electronics &amp; Communication/ Telecommunication/ E&amp;TC/ Electronics and Instrumentation) OR M.Sc. (Electronics/Physics with specialization in Electronics)</p> <p><b>AND</b></p> <p>02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p><b>Desirable:</b> M.E./M.Tech. (ECE/Microwave/RF &amp; Microwave/Microwave &amp; RADAR/Communication Engg.)</p> <p><b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b></p>	35 Years	<p><b>For PA-II</b></p> <p>*₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules</p> <p><b>For PA-I</b></p> <p>#₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules</p>
3207	<p>GAP 6521</p> <p>Design and development of Infrared based highly selective gas sensors array for environment air quality monitoring</p>	<p>Semiconductor Process Technology Group (SPTG)</p>	<p>Junior Research Fellow &amp; 01</p>	<p>M.Sc. (Electronics/ Physics/ Applied Physics/ Applied Electronics/ E&amp;I) or equivalent <b>OR</b> B.E./B.Tech./M.E./M.Tech. (EEE/ Electronics/ EE/ ECE/ Electronics &amp; Instrumentation/ Optoelectronics/ Microelectronics/Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI Design/ Instrumentation) or equivalent</p> <p><b>AND</b></p> <p>selected through a process described through any one of the following:</p>	35 Years	<p>#₹31,000/- + HRA as per rules</p>

				<p>(a) Scholars who are selected through National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.</p> <p>(b) The selection process through National Level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC, IISER etc.</p>		
3208	<p>MMP-015201 (WP-2.1.1)</p> <p>Design and development for band-pass filter for FMCW RADAR</p>	<p>Semiconductor Sensors and Microsystems Group (SSMG)</p>	<p>Project Associate-II/ Project Associate-I &amp; 01</p> <p><b><i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i></b></p>	<p>B.E./B.Tech./M.Sc. (Physics/ Electronics/ Communication/ Instrumentation) or equivalent</p> <p><b>AND</b></p> <p>02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p><b>Desirable:</b> M.E./M.Tech. (Electronics/ Nanotechnology/ Material Science/ Instrumentation /Microwave/ Electronics &amp; Communication) or equivalent</p> <p><b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b></p>	35 Years	<p><b>For PA-II</b></p> <p>*₹35,000/- + HRA as per rules</p> <p><b>OR</b></p> <p>*₹28,000/- + HRA as per rules</p> <p><b>For PA-I</b></p> <p>#₹31,000/- + HRA as per rules</p> <p><b>OR</b></p> <p>#₹25,000/- + HRA as per rules</p>
3209	<p>MMP-015201 (WP-2.1.2)</p> <p>FPGA based chirp generator for FMCW RADAR</p>	<p>Advanced Information Technologies Group (AITG)</p>	<p>Project Associate-II/ Project Associate-I &amp; 01</p> <p><b><i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i></b></p>	<p>B.E./B.Tech./M.Sc. (Physics/ Electronics/Communication/ Instrumentation) or equivalent</p> <p><b>AND</b></p> <p>02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services</p> <p><b>Desirable:</b> M.E./M.Tech. (Electronics/ Nanotechnology/ Material Science/ Instrumentation/ Microwave/ Electronics &amp; Communication) or equivalent</p> <p><b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b></p>	35 Years	<p><b>For PA-II</b></p> <p>*₹35,000/- + HRA as per rules</p> <p><b>OR</b></p> <p>*₹28,000/- + HRA as per rules</p> <p><b>For PA-I</b></p> <p>#₹31,000/- + HRA as per rules</p> <p><b>OR</b></p> <p>#₹25,000/- + HRA as per rules</p>

3210	MMP-015201 (WP-2.1.3)  Development of signal processing algorithms for FMCW RADAR	Advanced Information Technologies Group (AITG)	Project Associate-II/ Project Associate-I & 01  <i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i>	B.E/B.Tech. (CSE/ IT/ ECE/ Robotics/EE/EEE/E&I/ Instrumentation/ Mechanical/ Manufacturing) or equivalent <b>OR</b> M.Sc. (Electronics/Computer Science/IT) or equivalent  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Desirable:</b> Working experience and domain knowledge of signal processing, Image Processing, AI, Machine learning, Deep learning, Embedded System, Data fusion with working knowledge of C++/Python  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules
3211	MMP-015201 (WP-2.2)  Design and Development of Un-cooled Compact Pyroelectric-based THz Detector	Semiconductor Process Technology Group (SPTG)	Project Associate-II/ Project Associate-I & 01  <i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i>	B.E./ B.Tech./M.Sc. in Electronics & Communication/Electronics/ Electrical and Electronics/ Electrical/Instrumentation/ Physics/Applied Physics/ Optoelectronics/ Nanotechnology/ Microelectronics/VLSI Design or equivalent  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Desirable:</b> Knowledge of semiconductor device design and simulation  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules

3212	MMP-015201 (WP-2.3)  Design and Development of high-power THz transmission line waveguide products	High Power Microwave Systems Group (HPMSG)	Project Associate-I & 01	B.E./ B.Tech./M.Sc. (Electronics/ Chemical Technology/ ECE/ EEE/ Mechanical / Computer Science/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/ Mechatronics/ Material Science/ Physics/ Chemistry) or equivalent	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3213	MMP-015201 (WP-2.4)  Design and development of GaN HEMT for THz RADAR and communication	Semiconductor Process Technology Group (SPTG)	Project Associate-I & 01	B.E./ B.Tech./M.Sc. (Electronics/ ECE/ EEE / RPE /Physics) or equivalent.  <b>Preferable:</b> Knowledge of semiconductor device design and simulation (compound semiconductor), semiconductor fabrication, GaN-HEMT/RF devices, Synopsys/ADS etc.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3214	MMP-015201 (WP-2.4)  Design and development of GaN HEMT for THz RADAR and communication	Semiconductor Process Technology Group (SPTG)	Project Associate-II/ Project Associate-I & 01  <b>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</b>	B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE / RPE /Physics) or equivalent.  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Desirable:</b> M.E./M.Tech (Electronics/ Electronics & Communication/EE)  <b>Preferable:</b> Knowledge of semiconductor device design and simulation (compound semiconductor), semiconductor fabrication, GaN-HEMT/RF devices, Synopsys/ADS etc.  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules
3215	MMP-015201 (WP-2.5)  Development of Micro-Electro Mechanical Switches for Terahertz Technologies	Semiconductor Process Technology Group (SPTG)	Project Associate-I & 01	B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/Mechatronics/MEMS/ Physics) or equivalent.  <b>Desirable:</b> Knowledge of RF design, HFSS, ADS etc.	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules

3216	MMP-015201 (WP-2.5)  Development of Micro-Electro Mechanical Switches for Terahertz Technologies	Semiconductor Process Technology Group (SPTG)	Project Associate-II/ Project Associate-I & 01  <i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i>	B.E./ B.Tech./ M.Sc. (Electronics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/Mechatronics/ MEMS/ Physics) or equivalent.  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services <b>Desirable:</b> Knowledge of RF design, HFSS, ADS etc.  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules
3217	MMP-015201 (WP-2.6)  Development of MEMS-Based Terahertz Detector for Imaging Applications	High Frequency Devices and System Group (HFDSG)	Project Associate-II/ Project Associate-I & 01  <i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i>	B.E./ B.Tech/ M.Sc (Electronics/Physics/MW Electronics/ Applied Physics/ Optics/ Laser/ Nanotechnology/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ BioElectronics/ Microelectronics/ VLSI Design/ Instrumentation) or equivalent.  <b>AND</b>  02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules
3218	MMP-015201 (WP-2.7)  Design and development of THz vacuum electronic amplifier	Vacuum Electron Devices Group (VEDG)	Project Associate-II/ Project Associate-I & 01  <i>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</i>	B.E./ B.Tech. (Electronics & Communication/Telecom./ Electronics & Telecom./ Electronics and Instrumentation) or equivalent OR M.Sc.(Electronics/Physics with Specialization in Electronics) or equivalent  <b>AND</b> 02 years experience in Research and Development in Industrial and	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules

			<b>position will be considered)</b>	Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Desirable:</b> M.E./M.Tech. (ECE/Microwave/RF & Microwave/Microwave & RADAR/Communication Engg.) <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>		<b>OR</b> #₹25,000/- + HRA as per rules
3219	MMP-015201 (WP-2.8)  Design and development of antenna for THZ RADAR	High Frequency Devices and Systems Group (HFDSG)	Project Associate-II/ Project Associate-I & 01  <b>(Note: If suitable candidate is not found for PA-II position then the candidate for PA-I position will be considered)</b>	B.E/B.Tech. (Electronics & Comm./Telecom./Electronics & Telecom./Electronics and Instrument.) or equivalent OR MSc. (Electronics/Physics with specialization in Electronics) or equivalent  <b>AND</b> 02 years experience in Research and Development in Industrial and Academic Institutions or Science and Technology Organisation and Scientific activities and services  <b>Desirable:</b> M.E./M.Tech. (ECE/Microwave/RF & Microwave/Microwave & RADAR/Communication Engg.)  <b>Candidates with above qualification without experience can also apply. They shall be eligible for the position of Project Associate-I.</b>	35 Years	<b>For PA-II</b> *₹35,000/- + HRA as per rules <b>OR</b> *₹28,000/- + HRA as per rules  <b>For PA-I</b> #₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules
3220	IHP202405  Design and Development of palladium based micro-thermal hydrogen sensor system for leak detection	Semiconductor Sensors and Microsystems Group (SSMG)	Project Associate-I & 01	B.E./ B.Tech. in Electronics Engineering/ Electronics and and Communication Engineering/ Electronics and Instrumentation/ Electrical Engineering or equivalent <b>OR</b> M.Sc. in Electronics or equivalent	35 Years	#₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules
3221	IHP202406  Design and Development of MEMS density sensor for liquids	Semiconductor Sensors and Microsystems Group (SSMG)	Project Associate-I & 01	B.E./ B.Tech. in Electronics Engineering/ Electronics and and Communication Engineering/ Electronics and Instrumentation/ Electrical Engineering or equivalent <b>OR</b> M.Sc. in Electronics or equivalent	35 Years	#₹31,000/- + HRA as per rules <b>OR</b> #₹25,000/- + HRA as per rules

3222	CLP-3162 Development of Portable, IoT Enabled Waste Water Quality Estimation System	Advanced Information Technology Group (AITG)	Project Associate-I & 01	B.E./ B.Tech./M.Sc. (Electronics Instrumentation/Instrumentation & Control System/Electrical Engg./EEE/CS/CE or equivalent)	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3223	GAP-6221 Bio-mimetic and photo technologies designed for low-cost purification and recycling of water	Advanced Information Technology Group (AITG)	Project Associate-I & 01	B.E./ B.Tech./M.Sc. (Electronics Instrumentation/Instrumentation & Control System/Electrical Engg./EEE/CS/CE or equivalent)	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules

# For Project Associate – I:

(i) ₹31,000/- + HRA to Scholars who are selected through-

- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
- A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹25,000/- + HRA for others who do not fall under (i) above.

\* For Project Associate – II:

(i) ₹35,000/- + HRA to Scholars who are selected through-

- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
- A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹28,000/- + HRA for others who do not fall under (i) above.

\*\* Increment of 15% for 3 years of experience with maximum ceiling of 4 such revisions i.e. upto 12 years of experience.

**For AcSIR Ph.D. (Engineering/IDDP) Students**

- Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.***
- Those who are having fellowship, they are not eligible to apply.***

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

<b>For Recruitment queries:</b>  <b>Sh. Rajan Tirkey</b> <b>Section Officer</b> <b>CSIR-CEERI, Pilani</b> <b>Email: <a href="mailto:recruitment@ceeri.res.in">recruitment@ceeri.res.in</a></b> <b>Phone:01596-252275</b>	<b>For Technical queries:</b>  <b>Sh. Sanjeev Kumar</b> <b>Advanced Information Technologies Group</b> <b>CSIR-CEERI, Pilani</b> <b>Email: <a href="mailto:sanjeevkumar@ceeri.res.in">sanjeevkumar@ceeri.res.in</a></b> <b>Phone:01596-252282</b>
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**Other Conditions:**

1. Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ Project Fellow/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ Project Associate-I/II etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure up to remaining period till completion of five years.
2. Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate- I, Project Assistant, Project Associate-I/II, JRF/SRF etc. shall not exceed 5 years in any case. The total tenure of five years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as RA, Project Assistant, Project Associate-I/II etc. or as any other designation of equal status.
- 3. All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**
4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Scientific Administrative Assistant/Project Assistant, Project Associate-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PwBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
- 7. Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:**
  - a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
    - i. 10<sup>th</sup> Standard
    - ii. 12<sup>th</sup> Standard
    - iii. Diploma Degree
    - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
    - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.
  - b. Date of Birth Proof (Any valid certificate having DoB such as 10<sup>th</sup> Standard, DoB Certificate, etc.)
  - c. Category certificate (in case of SC/ ST/ OBC/ PwBD/ EWS candidates)
  - d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)

- e. Experience certificate (Mandatory for the posts where experience is essential qualification)
- f. Other certificates (If applicable)
8. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
9. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
10. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.
11. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
12. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.
13. The number of positions may be increased or decreased as per the requirements.
14. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the engagement of above Project Staff as amended from time to time.

**The last date & time for applying applications is 24.06.2024 till 05:00 P.M. Application received after last date will not be entertained.**

**SECTION OFFICER**