

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)

List of Shortlisted candidates for the Post Code 3182 & Project No. GAP-6521
(Advt. No. PF-01/2024)

Following Candidates have been shortlisted for one position of Junior Research Fellow against the Post Code 3182 & Project No. GAP-6521, advertised vide Advt. No. PF-01/2024, for online interview (through MS Teams) as per the schedule given below:

Sr. No.	Application No.	Name of Candidate	Date of Interview	Time of Interview
1.	1711961591	Ankit Tomar	Friday 17th May, 2024	10:00 AM Onwards
2.	1711959054	Rashmi Bala		
3.	1711941441	Pankaj Garsa		
4.	1711912506	Ranjan Verma		
5.	1711731486	Ayush Purohit		
6.	1711536398	Mohd Irfan		
7.	1711260245	Ayushi Kirti Singh		

Criteria adopted for shortlisting:

B.E./B.Tech./M.Sc./M.E./M.Tech or equivalent with a percentage of marks $\geq 60\%$ or CGPA ≥ 6.5
AND GATE/ NET

Candidates must join the said meeting at least 15 minutes before through MS Teams Link. They will be admitted for interview according to their turn as per attached list of shortlisted candidates. The candidates have to wait for their turn, if all available candidates interviewed and no candidate available in MS Teams lobby, then selection committee meeting will be over.

Important Information:

1. The shortlisted candidates have to appear for online interview as per the date and time given above.
1. The link is being sent to their email IDs.
2. The candidates have to join the interview with their application number followed by - and their full name. Example: If the application Number is **1234567890** and Name is **Ramesh Kumar**, then join with **1234567890 - Ramesh Kumar**.
3. The shortlisting has been done on the basis of information provided by the candidates in the online application. If any information found incorrect, the candidature will be rejected without further correspondence.

SECTION OFFICER