

“Online Interview”

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

Online applications are invited from candidates for the Engagement of Project Staff.

Details of Vacancies & Eligibility

Post Code	Project No.	Area/Group	Name & Number of Positions	Essential Qualification as on the last date of receipt of online applications i.e. 29.12.2023	Age	Consolidated Emoluments Per month (fixed)
3164	HCP-0053 (WP-5)	Integrated Circuit and System Group(ICSG)	Scientific Administrative Assistant/Field Worker & 01	Science Graduate Remarks: Candidates with an electronics subject background will be preferable.	50 Years	₹18,000/- + HRA as per rules
3165	GAP-3417	Integrated Circuits and Systems Group(ICSG)	Junior Research Fellow & 01	B.E./ B.Tech. (CSE/ IT/ ECE/ Robotics/ EE/ EEE/ E&I/ Instrumentation or M.Sc. (Electronics/Computer Science/ IT) or equivalent Desirable: Working experience and domain knowledge of Image Processing, AI, Machine Learning, Deep Learning, Embedded System, Data fusion with working knowledge of C++/ Python	35 Years	₹31,000/- + HRA as per rules OR ₹25,000/- + HRA as per rules
3166	HCP-0042 (WP-6.1)	Advanced Information Technologies Group (AITG)	Project Associate-I & 01	B.E./B.Tech. in Electronics and Electrical Engineering(EEE)/ Electronics and Instrumentations/ Electronics/Electronics and Communication Engineering/ Computer Science & Engineering/Information Technology or equivalent Desirable: Working experience and domain knowledge of Python coding, Machine learning and deep learning, software development Scope of Project: Development of Machine Learning/Deep Learning/ AI Algorithms (with domain knowledge of Python), AI Infrastructure Management.	35 Years	₹31,000/- + HRA as per rules OR ₹25,000/- + HRA as per rules

3167	HCP-0042 (WP-2.4)	Advanced Information Technologies Group (AITG)	Senior Project Associate & 01	<p>B.E./ B.Tech. in Electronics and Electrical Engineering (EEE) / Electronics and Instrumentations/ Electronics/Electronics and Communication Engineering/ Computer Science & Engineering or equivalent</p> <p style="text-align: center;">AND</p> <p>Four years of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations and Scientific Activities and Services in the areas of AI, Machine Learning Algorithms and Deep Learning Algorithms (with domain knowledge of Python)</p> <p style="text-align: center;">OR</p> <p>M.E./M.Tech/M.S. in Electronics and Electrical Engineering(EEE)/ Electronics and Instrumentations / Advanced Electronics Engineering / Electronics / Electronics and Communication Engineering/ Computer Science & Engineering/ Embedded Systems or equivalent</p> <p style="text-align: center;">AND</p> <p>Two years of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations and Scientific Activities and Services in the areas of AI, Machine Learning Algorithms and Deep Learning Algorithms (with domain knowledge of Python)</p> <p style="text-align: center;">OR</p> <p>Ph.D. (Science/Engineering/ Technology) in the areas of AI, Machine Learning Algorithms and Deep Learning Algorithms</p> <p>Desirable: Working experience and domain knowledge of Python coding, Machine learning and deep learning, software development</p>	40 Years	₹42,000/- + HRA as per rules
3168	HCP-0042 (WP-5.1)	Advanced Information Technologies Group (AITG)	Project Associate-I & 01	<p>B.E./B.Tech. in Electronics and Electrical Engineering(EEE)/ Electronics and Instrumentations/ Electronics/Electronics and Communication Engineering/ Computer Science & Engineering/Information Technology/Electronics and</p>	35 Years	<p>#₹31,000/- + HRA as per rules</p> <p style="text-align: center;">OR</p> <p>#₹25,000/- + HRA as per rules</p>

3172	HCP-0101	PME	Project Associate-I & 01	B.E./B.Tech. in Computer Science/IT/Electronics or M.Sc. in Electronics or equivalent	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3173	HCP-0101	PME	Project Associate-II & 01 (Note : If suitable candidate for PA-II position not found, then suitable candidate for the position of PA-I may be considered from Post Code 3172)	B.E./B.Tech. in Computer Science/IT/Electronics or M.Sc. in Electronics or equivalent AND Two years of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations Desirable: M.E./M.Tech. in Computer Science/Electronics Preferable: Experience in Programming, Animation, Database creation ARVR, front End Desing, Microcontroller based App Development and related virtual/algorithm development	35 Years	*₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules
3174	NWP-100	Skill Development Unit (CSIR-CEERI, Jaipur Campus)	Project Associate-I & 02	B.E./B.Tech/M.Sc. (CSE/ECE/EEE/IT/Electronics & Instrumentation/ Electronics) or equivalent Desirable: Working knowledge of MATLAB & JAVA. Should know about electronic circuit design. Should have a good understanding of power point presentations and MS-Excel and should possess excellent communication skills	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3175	NWP-100	Skill Development Unit (CSIR-CEERI, Jaipur Campus)	Project Associate-II & 01	B.E./B.Tech. (VLSI Design/ MEMS/ Electronics/Optoelectronics/ Photonics/Material Science) or M.Sc.(Physics/Electornics) or equivalent AND Two years of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisation and Scientific Activities and Services Desirable: Programming skills in Arduino, Circuit design, Simulation and PCB	35 Years	*₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules

For Project Associate – I and Junior Research Fellow:

(i) ₹31,000/- + HRA to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc and IISER etc.

(ii) ₹25,000/- + HRA for others who do not fall under (i) above.

				Communication or M.Sc./MCA or equivalent Scope of Project: Development of Machine Learning/Deep Learning/ AI Algorithms (with domain knowledge of Python), AI Infrastructure Management.		
3169	GAP-6107	Advanced Electronic Systems Area	Project Associate-II & 01	B.E./B.Tech./M.Sc. (ECE/EEE/EE/ Instrumentation/Electrical/ Electronics/ Power Engineering/ Renewable Energy) or equivalent AND Two years of experience in R&D in Industrial and Academic Institutions or Science and Technology Organisation Desirable : Working experience and domain knowledge of areas of power electronics or circuit design or renewable systems	35 Years	*₹35,000/- + HRA as per rules OR *₹28,000/- + HRA as per rules
3170	GAP-3141	CSIR-CEERI Jaipur Campus	Junior Research Fellow & 01	B.E./B.Tech. (Electrical Engineering/Electrical and Electronics Engineering/Electronics Engineering/Instrumentation Engineering or equivalent) OR M.E./M.Tech (Power Electronics & Drives/High Voltage Engineering/ Electrical Engineering) or equivalent Desirable : Knowledge in PCB Design, Microcontroller Programming, MATLAB/PSIM/ LTSPICE simulation desirable Scope of Project: <ul style="list-style-type: none"> • Project will involve design and development of Pulsed Power Supplied for Non-Thermal Processing Applications • Design, Development of Auxiliary circuits • Hardware Testing and Validation 	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules
3171	GAP-3244	Semiconductor Device Area	Project Associate-I & 01	B.E./B.Tech. in Electronics/ Electronics and Communication/ Electronics and Instrumentation/ Electronics and Electrical Engineering or M.Sc. in Electronics or equivalent	35 Years	#₹31,000/- + HRA as per rules OR #₹25,000/- + HRA as per rules

* For Project Associate – II and Senior Research Fellow:

(i) ₹35,000/- + HRA to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹28,000/- + HRA for others who do not fall under (i) above.

Note:

If any candidate wishes to apply for two or more projects or different positions in a project, then such candidates have to apply separately.

For AcSIR Ph.D. (Engineering/IDDP) Students

1. *Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.*
2. *Those who are having fellowship, they are not eligible to apply.*

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

The last date & time for applying applications is **29.12.2023 till 05:00 P.M.** Application received after last date will not be entertained.

For Recruitment queries: Sh. Rajan Tirkey Section Officer CSIR-CEERI, Pilani Email: recruitment@ceeri.res.in Phone:01596-252275	For Technical queries: Sh. Sanjeev Kumar Advanced Information Technologies Group CSIR-CEERI, Pilani Email: sanjeevkumar@ceeri.res.in Phone:01596-252282
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Other Conditions:

1. Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ Project Fellow/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ Project Associate-I/II etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure up to remaining period till completion of five years.
2. Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate- I, Project Assistant, Project Associate-I/II, JRF/SRF etc. shall not exceed 5 years in any case. The total tenure of five years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as RA, Project Assistant, Project Associate-I/II etc. or as any other designation of equal status.
3. **All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**

4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Scientific Administrative Assistant/Project Assistant, Project Associate-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PwBD/EWS candidates may be given preference over General candidates so as to ensure their representation.

7. Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:-

- a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
 - i. 10th Standard
 - ii. 12th Standard
 - iii. Diploma Degree
 - iv. Graduation Degree (B.A./B.Com./BBA/BCA/ B.Sc./B.E./B.Tech. etc.)
 - v. Post-Graduation (M.Sc./M.E./M.Tech./MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/DGPA should be clearly displayed on any one of the certificate. If certificate doesn't have proper percentage/CGPA/DGPA/Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DoB such as 10th Standard, DoB Certificate, etc.)
 - c. Category certificate (in case of SC/ST/OBC/PWD/EWS candidates)
 - d. NET/GATE/Any other equivalent exam qualification certificate (If applicable)
 - e. Experience certificate (Mandatory for the posts where experience is essential qualification)
 - f. Other certificates (If applicable)
8. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
 9. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
 10. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/documents and other information found correct.
 11. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
 12. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empanelled candidate at any stage.

13. The number of positions may be increased or decreased as per the requirements.

14. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/ CSIR/ CEERI for the engagement of above Project Staff as amended from time to time.


CONTROLLER OF ADMINISTRATION 03