

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE
PILANI - 333 031 (RAJASTHAN)

No.4/2-3(4)/2022/E-I

Date : 12.10.2023

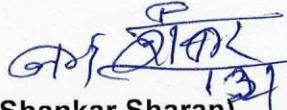
OFFICE MEMORANDUM

**Sub: Eligibility of regular AcSIR Ph.D. (Engineering/IDDP) Students
for engagement as Project Staff on R&D Projects of the Institute.**

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It is hereby notified that the regular Ph.D. (Engineering/IDDP) students who does not have any fellowship can apply for project staff position in a R&D project of the Institute during his/her Ph.D. tenure only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director.

This is for information and necessary compliance by all concerned.


(Jay Shankar Sharan)
Controller of Administration

Copy to :

1. Dr. Mahanth Prasad,
Co-ordinator, AcSIR
2. Director's Secretariat
3. All Heads of Section/Division/Units
4. Head, AITG -- Kindly upload this OM on CSIR-CEERI website.