

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI

सं. 4/2-3(4)/2022-स्थ. I

Date: 18.09.2023

**OFFICE MEMORANDUM**

**Result for the post of Junior Research Fellow/Project Associate-I advertised vide  
Advt. No. PF- 05/2023.**

Based on the recommendations of the Selection Committee & performance of candidates in Online Interview (held through MS Teams), the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement of Junior Research Fellow/Project Associate-I that are empanelled in order of merit.

The details are as under: -

**POST CODE- 3154**

Name of Position: Junior Research Fellow/Project Associate-I  
Project No.: GAP-6518  
Number of Position: 1

Order of Merit	Application No.	Name of Candidate Included in Panel	Emolument(s)
1.	1693567560	Mr. Mohd Faisal Khan	₹25,000/- p.m. + HRA as per rules
2.	1693559288	Mr. Md Arbaaz Fazal	₹25,000/- p.m. + HRA as per rules
3.	1693546576	Mr. Rishav Pandey	₹31,000/- p.m. + HRA as per rules
4.	1693503369	Mr. Adarsh Kumar Mishra	₹25,000/- p.m. + HRA as per rules

  
18/09/2023  
**SECTION OFFICER**

**Note:-**

The above panels will remain valid for a period of One Year from the date of issue of result or upto the duration of the project, whichever is earlier. The above panels does not confer any right on the empanelled candidates for engagement as Project Staff positions. The engagement to the Project Staff positions from the above panels will be made subject to the availability of vacancies in the Projects.