

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI

Date: 15.09.2023

सं. 4/2-3(4)/2022-स्था. I

OFFICE MEMORANDUM

Result for the post of Junior Research Fellow advertised vide Advt. No. PF- 05/2023.

Based on the recommendations of the Selection Committee & performance of candidates in Online Interview (held through MS Teams), the Director, CSIR-CEERI, Pilani has been pleased to approve the engagement of Junior Research Fellow that are empanelled in order of merit.

The details are as under: -

POST CODE- 3149

Name of Position: Junior Research Fellow
Project No.: GAP-3417
Number of Position: 01

Order of Merit	Application No.	Name of Candidate Included in Panel	Emolument(s)
1.	1693329067	Mr. Sudhanshu Sharma	₹25,000/- p.m. + HRA as per rules
2.	1693545233	Mr. Rishav Pandey	₹31,000/- p.m. + HRA as per rules


15/09/2023
SECTION OFFICER

Note:-

The above panels will remain valid for a period of One Year from the date of issue of result or upto the duration of the project, whichever is earlier. The above panels does not confer any right on the empanelled candidates for engagement as Project Staff positions. The engagement to the Project Staff positions from the above panels will be made subject to the availability of vacancies in the Projects.