

About Karyashala

High - End Workshop

The science and Engineering Research Board (SERB), a statutory body under the Department of Science and Technology, Government of India, has inaugurated an Inter-Ministerial Initiative, 'Accelerate Vigyan (AV) Scheme' to provide a big push to high-end scientific research and prepare scientific manpower which can venture into research careers and knowledge-based economy. AV aims to expand the research base in the country with three broad goals-consolidation/aggregation of all scientific training programs, initiating high-end Orientation Workshops, and creating opportunities for Research Internships. The AV scheme aims to boost Research & Development in the country by enabling and grooming potential PG and Ph.D. students by developing dedicated research skills in selected areas/ disciplines/fields through High-End orientation workshops 'KARYASHALA'. This will be especially important for researchers with limited opportunities to access such learning capacities/facilities/infrastructure.

Hands-on Training on Semiconductor Device Fabrication



Nov 08-19, 2022

sponsored
by

**SCIENCE & ENGINEERING
RESEARCH BOARD (SERB)
Department of Science &
Technology, Government of
India**

Organised at

**CSIR - Central Electronics
Engineering Research
Institute**



Major Highlights

Hands-on training on Semiconductor Processing, Device Fabrication, and Characterization for P-N Junction Diode & MOS Capacitor



About CSIR-CEERI

CSIR - Central Electronics and Engineering Research Institute (CSIR-CEERI) is a premier research and development institute under the Council of Scientific and Industrial Research (CSIR), Govt. of India. CSIR-CEERI has made a significant impact on electronics and allied engineering, especially with regard to Microwave Devices, Sensor Technologies, VLSI Design, and Embedded Systems.



Name:

Fathers Name:

Date of Birth:

Gender:

Category: SC/ST/OBC/Gen

Address:

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Contact No:

Email:

Student Enrollment No:

University/ Institute Name:

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Signature of HOD/
Supervisor
(with seal)

Signature of
the Participant

Specialized lectures on device fabrication techniques, device Characterization and device feasibility testing.

Hands-on practical exposure on the state-of-the-art equipment/machinery.

Hands on training on

1. Photolithography
2. Metallization
3. Thermal Oxidation
4. Spin Coaters
5. Metal Lift Off
6. Dry and Wet etching
7. Deposition systems (PVD, CVD etc)
8. Thermal Oxidation
9. Packaging of device
10. Device Characterisation
11. Device Feasibility testing

No registration Fee

The total number of participants will be 25.
TA/DA will be provided to all selected participants.
Accommodation will be provided.
Students should enclose a recommendation letter from the HOD/Supervisor, along with registration form

Eligibility: Ph.D student / M. Tech Student (ECE or equivalent)/ M. Sc (Phy/ Electronics)

Last date of registration: 24 Oct 2022 (4:00 PM IST)
Notification to selected candidates: 25 Oct 2022
Training Duration: 08- 19, Nov 2022.

UNIQUE LEARNING OPPORTUNITY

Chief Patron: Dr. P. C. Panchariya
Director, CSIR-CEERI, Pilani

Organizing Committee:

- Dr. Abhijit Karmakar, Chief Scientist
- Dr. Suchandan Pal, Chief Scientist
- Dr. Kuldip Singh, Sr. Pr. Scientist
- Mr. Ashok Chouhan, Pr. Scientist
- Mr. Pramod Tanwar, Pr. Scientist

Event Organiser

- Dr. Vijay Chatterjee, Sr. Scientist

Resource Persons:

- Dr. Manish Mathew, Pr. Scientist
- Dr. S. Santosh Kumar, Pr. Scientist
- Dr. Nikhil Suri, Pr. Scientist
- Dr. Dheeraj Kharbanda, Pr. Scientist
- Dr. Pijus Kundu, Pr. Scientist
- Dr. Rahul Prajesh, Sr. Scientist
- Dr. Amit Kumar, Sr. Scientist
- Dr. Ranjan K Maurya, Sr. Scientist
- Dr. T. Eshwar, Sr. Scientist
- Mr. Prateek Kothari, Sr. Technical Officer
- Mr. Arvind K Singh, Sr. Technical Officer
- Mr. Prem Kumar, Technical Officer
- Mr. Rama Kant Sharma, Technical Officer
- Mr. Priyavrat Parjapat, Technical Officer
- Dr. Prashant Sharma, Technical Officer
- Mr. Bhoopendra K Kushwaha, Technical Officer
- Mr. Anand K Upadhyay, Technical Officer
- Mr. Banwari Lal, Sr. Technician
- Mr. Jitendra Bhargawa, Technician
- Mr. Dhirendra Kumar, Sr. Technical Officer
- Mr. Deepak K Panwar, Technical Officer
- Dr. Maninder Kaur, Sr. Technical Officer
- Mr. Gajendra Singh Meena, Technical Officer
- Mr. Supriyo Das, Sr. Technical Officer
- Mr. Ashok Sharma, Technician
- Mr. Bhawani Shankar Jangir, Technician
- Mr. Yetendra, Technician

How to reach CSIR-CEERI, Pilani

CSIR-CEERI, Pilani is about 1.5 km from Pilani Bus stand. The Nearest Railway station to Pilani is Loharu (Haryana) For more information please visit: <https://www.ceeri.res.in/visit-pilani/>

In case of any query, please contact:
1. Mr. Rama Kant Sharma: +91- 9001388154
2. Mr. Anand Upadhyay: +91- 7568565038



Send your registration form duly signed by HOD/Supervisor to vc@ceeri.res.in

Alternately you can also register at:
<https://forms.gle/zmNCZxyDeddY3Lht7>