



सीएसआईआर-केन्द्रीय इलेक्ट्रॉनिकी अभियांत्रिकी अनुसंधान संस्थान
CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE
(विज्ञान तथा प्रौद्योगिकी मंत्रालय / MINISTRY OF SCIENCE & TECHNOLOGY, भारत सरकार / GOVT. OF INDIA)
पिलानी, राजस्थान (भारत)/Pilani, Rajasthan - 333031 (INDIA)



Dated: 21.08.2020

Corrigendum to Advt. No. PF-01/2020

Attention is invited towards Advt. No. PF-01/2020, issued for Engagement of Project Staff. It is notified to all concerned that the Stipend in respect of Project Associate – I (Post Code - 0108) and Project Associate – II (Project Code - 0109 & 0110) is modified as follows:

Post	Stipend
Project Associate-I	(i) ₹31,000/- + HRA to Scholars who are selected through (a) National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions. (b) ₹25,000/- + HRA for others who do not fall under (i) above.
Project Associate-II	(i) ₹35,000/- + HRA to Scholars who are selected through (a) National Eligibility Tests – CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions. (ii) ₹28,000/- + HRA for others who do not fall under (i) above.

The other details of the advertisement will remain the same.

SECTION OFFICER