

One-day Workshop on VLSI & Integrated Electronic System Design (VIESD)

February 26, 2019

Time	Program Details
09:15 AM – 09:45 AM	Inauguration
09:45 AM – 10:45 AM	Prof. D. K. Sharma (IIT Bombay)
10:45 AM – 11:30 AM	Smt. Sunita Verma (MeitY)
11:40 AM – 12:00 PM	Tea Break
12:05 PM – 12:45 PM	Dr. Arti Noor (CDAC)
12:45 PM – 01:30 PM	Prof. Chandra Shekhar (BITS Pilani)
01:30 PM – 02:30 PM	Lunch Break