

## Corrigendum

**Tender Reference Number:** 725/MLP-0027/IOT/6-Pur/2018

**Tender ID:** 2019\_CSIR\_21856\_1

The highlighted parts below should be considered as amendments in the Technical Specifications with reference to the Tender/ NIT as mentioned above.

### Annexure – A

#### **Detailed Specifications of PXI Controller**

<b>Sl. No.</b>	<b>Parameter</b>	<b>Requirements</b>
1.	CPU	Intel® Core™ i7 5700EQ (2.6 GHz quad-core processor)
2.	On-Die last level cache	6 MB
3.	Single-Channel 1600 MHz DDR3L RAM, PC3 12800	8 GB
4.	Hard Drive	320 GB Serial ATA
5.	Ethernet	Two 10/100/1000 Base T ports
6.	PXI Express 4 Link Configuration	x4, x4, x4, x4
7.	PXI Express 2 Link Configuration	x8 , x8
8.	GPIB (IEEE 488 Controller)	Yes
9.	Serial Port (RS-232)	Yes (1)
10.	Hi-Speed USB (2.0) Ports	Yes (4)
11.	SuperSpeed USB (3.0) Ports	Yes (2)
12.	ExpressCard/ 34 Slot	Yes
13.	PXI Trigger Bus Input/ Output	Yes
14.	Operating System	Windows 7 Professional