

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE
PILNIA (RAJASTHAN)

OFFICE MEMORANDUM

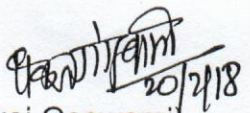
File No. PMBD/TRL/01/2018

Dated:20/02/2018

The Director, CSIR-CEERI has been pleased to constitute a committee for auditing and assessing Technology Readiness Level (TRL) of CSIR-CEERI technologies based on evidence/ presentation and discussion with the development team as under:-

- | | |
|----------------------------|--------------------|
| 1. Prof. Santanu Chaudhury | - Chairman |
| 2. Dr. S.C Bose | - Member |
| 3. Dr. R.K. Sharma | - Member |
| 4. Dr. Ajay Agarwal | - Member |
| 5. Dr. S. Ali Akbar | - Member, Convener |

This committee will bring out a Standard Operating Procedure/ Process for commercialization of CSIR-CEERI technologies for the above mentioned purposes.


(Pankaj Goswami)
Section officer

Copy to:

1. PS to Director
2. PS to C.O.A
3. All the members concerned
4. CEERI Website
5. Notice Board